



Surennoo HDMI Display Module Series

Model No.: SHD156A-19201080

USER MANUAL

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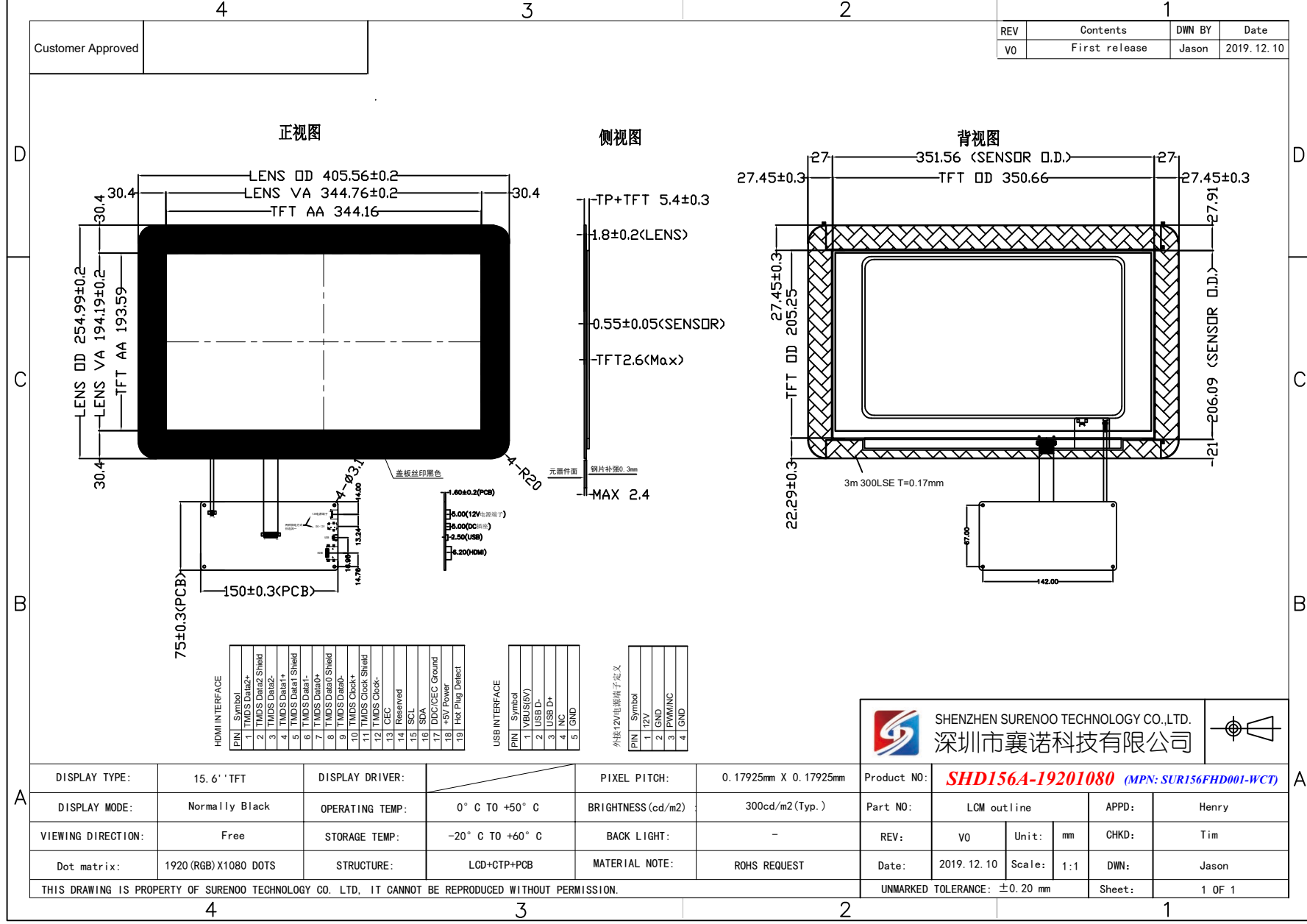
11、INSPECTION CRITERION



1、GENERAL INFORMATION

<i>Item of general information</i>	<i>Contents</i>	<i>Unit</i>
<i>LCD Display Size (Diagonal)</i>	<i>15.6</i>	<i>inch</i>
<i>LCD Display Type</i>	<i>TFT/TRANSMISSIVE</i>	<i>-</i>
<i>LCD Display Mode</i>	<i>Normally Black</i>	<i>-</i>
<i>Recommended Viewing Direction</i>	<i>ALL VIEW</i>	<i>o'clock</i>
<i>Module size (W×H×T)</i>	<i>405.56×254.99×5.40</i>	<i>mm</i>
<i>Active area (W×H)</i>	<i>344.16×193.59</i>	<i>mm</i>
<i>Number of pixels (Resolution)</i>	<i>1920RGB×1080</i>	<i>pixel</i>
<i>Pixel pitch (W×H)</i>	<i>0.17925×0.17925</i>	<i>mm</i>
<i>Color Pixel Arrangement</i>	<i>RGB Stripe</i>	<i>-</i>
<i>TOUCH Interface</i>	<i>USB</i>	<i>-</i>
<i>LCD Interface</i>	<i>HDMI</i>	<i>-</i>
<i>LCM Power consumption</i>	<i>-</i>	<i>mA</i>
<i>Color Numbers</i>	<i>6bit+FRC</i>	<i>-</i>
<i>Backlight Type</i>	<i>White LED</i>	<i>-</i>

2, EXTERNAL DIMENSIONS



3、ABSOLUTE MAXIMUM RATINGS

<i>Parameter of absolute maximum ratings</i>	<i>Symbol</i>	<i>Min</i>	<i>Max</i>	<i>Unit</i>
<i>Power supply voltage</i>	<i>DC12V</i>	-0.3	21	<i>V</i>
<i>Operating temperature</i>	<i>Top</i>	-10	50	°C
<i>Storage temperature</i>	<i>Tst</i>	-20	60	°C
<i>Humidity</i>	<i>RH</i>	-	90%(Max 60°C)	<i>RH</i>

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

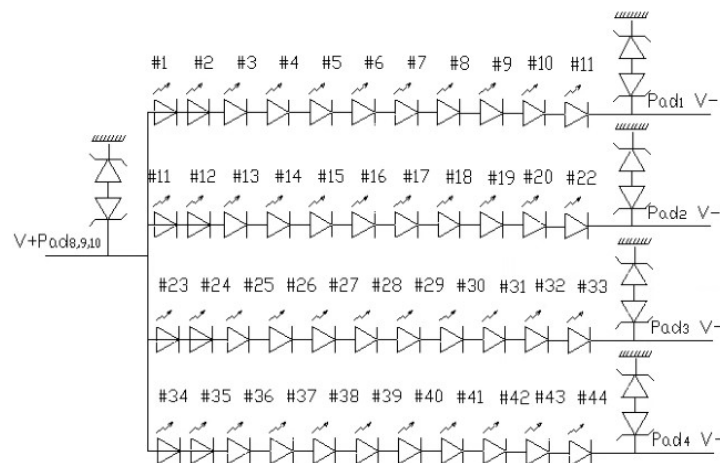
<i>Parameter of DC characteristics</i>	<i>Symbol</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
<i>Power Supply Voltage</i>	<i>DC12V</i>	-	12	-	<i>V</i>
<i>Power Supply Current</i>	<i>IDC</i>	-	-	-	<i>mA</i>

5、BACKLIGHT CHARACTERISTICS

<i>Item of backlight characteristics</i>	<i>Symbol</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>	<i>Remark</i>
Backlight supply voltage	<i>VBL</i>	5.0	12.0	21.0	<i>V</i>	
Current dissipation	<i>IBL</i>	-	-	-	<i>mA</i>	
PWM signal voltage	<i>VpwmH</i>	2.2	-	5.0	<i>V</i>	<i>BL_PWM_DIM</i>
	<i>VpwmL</i>	0	-	0.6	<i>V</i>	
Brightness Control Duty Ratio	<i>Duty</i>	1	-	100	%	
Brightness Control frequency	<i>fpwm</i>	100	-	10,000	<i>Hz</i>	
BL ON/OFF High voltage	<i>VcntH</i>	2.2	-	5.0	<i>V</i>	<i>BL_ENABLE</i>
BL ON/OFF Low voltage	<i>VcntL</i>	0	-	0.6	<i>V</i>	
Number of LED	-	-	11*4=44	-	<i>Piece</i>	-
LED Connection mode	<i>P/S</i>	-	Serial/Parallel	-	-	-
Lifetime of LED	-	15000	-	-	<i>hour</i>	Note2

Note:

- Note1: The LED Supply Voltage is defined by the number of LED at $T_a=25^{\circ}\text{C}$ and $I_f=21\text{mA}$.
- Note2: The LED lifetime define as the estimated time to 50% degradation of initial luminous. The LED lifetime could be decreased if operating I_f is larger than 21mA.
- Backlight circuit:



6、ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-optical characteristics		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	$\theta=0$ $\phi=0$ Ta=25°C	-	25	-	ms	FIG 1.	4
Contrast Ratio		CR		-	800	-	-	FIG 2.	1
Luminance uniformity		$\delta WHITE$		-	80	-	%	FIG 2.	3
Surface Luminance		Lv		-	300	-	cd/m2	FIG 2.	2
CIE (x, y) chromaticity	White	White x	$\theta=0$ $\phi=0$ Ta=25°C	0.283	0.311	0.343	-	FIG 2.	5
		White y	0.299	0.332	0.365				
Viewing angle range	$\phi=90(12\text{ o'clock})$		CR ≥ 10	80	85	-	deg	FIG 3.	6
	$\phi=270(6\text{ o'clock})$			80	85	-	deg		
	$\phi=0(3\text{ o'clock})$			80	85	-	deg		
	$\phi=180(9\text{ o'clock})$			80	85	-	deg		
NTSC ratio		-	-	68	72	-	%	-	-

Note 1. Contrast Ratio (CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{Contrast Ratio(CR)} = \frac{\text{Average Surface Luminance with all white pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}{\text{Average Surface Luminance with all black pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)}$$

Note 3. The uniformity in surface luminance ($\delta WHITE$) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note 4. The response time is defined as the LCD optical switching time interval between “White ” state and “Black ” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For more information see FIG 1.

Note 5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers’s ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

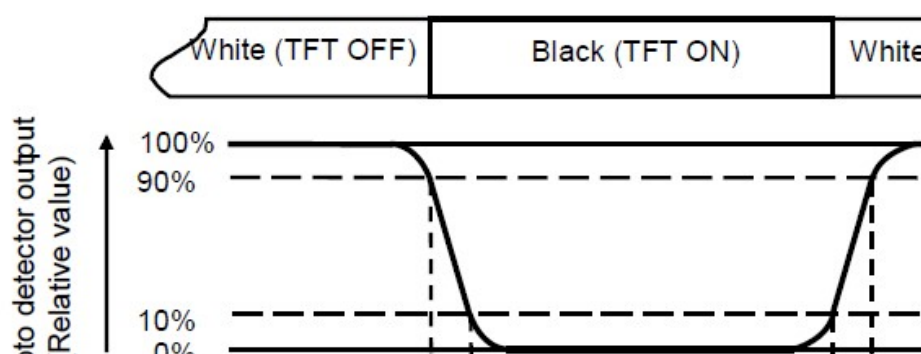


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

CIE (x , y) chromaticity

$A : H/6 ;$

$B : V/6 ;$

$H, V : \text{Active Area(AA) size}$

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

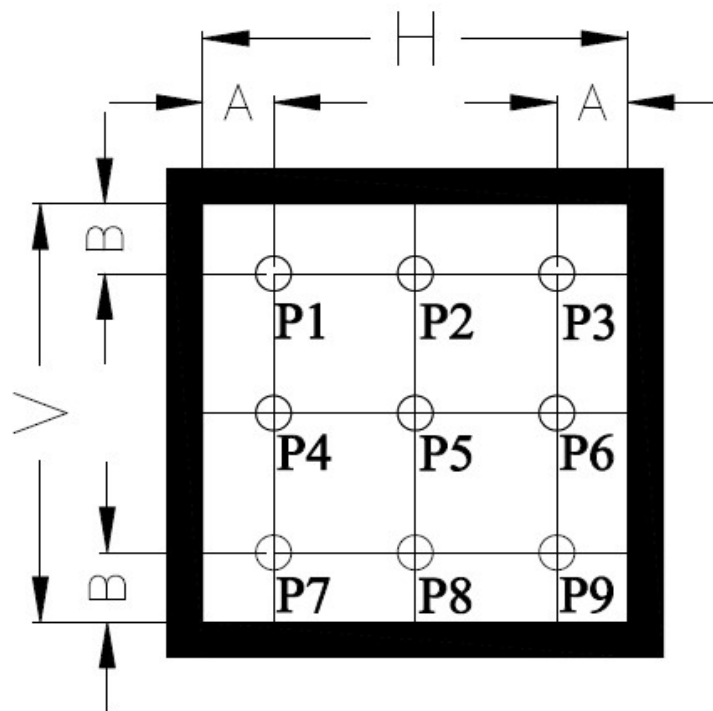
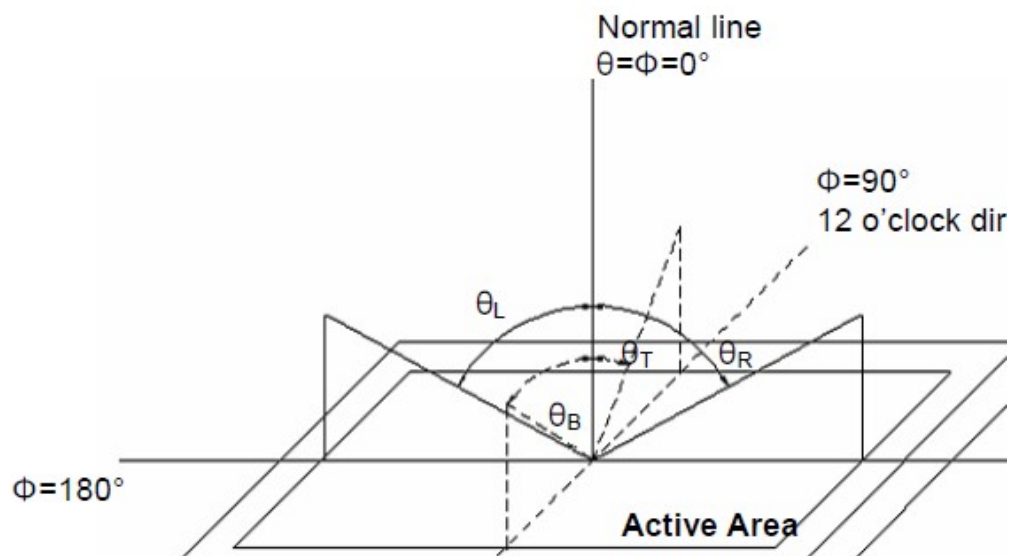


FIG.3. The definition of viewing angle



7、INTERFACE DESCRIPTION

A、**HDMI** Interface Description

NO.	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect



B、**USB TOUCH** Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power
2	D-	USB Data-
3	D+	USB Data+
4	NC	No connection
5	GND	Power Ground

C、**External POWER** Interface Description

NO.	Symbol	DESCRIPTION
1	12V	External power input(12V)
2	GND	Power Ground
3	PWM/NC	Brightness Control input signal(100~10,000Hz)
4	GND	Power Ground

8、**INPUT TIMING**

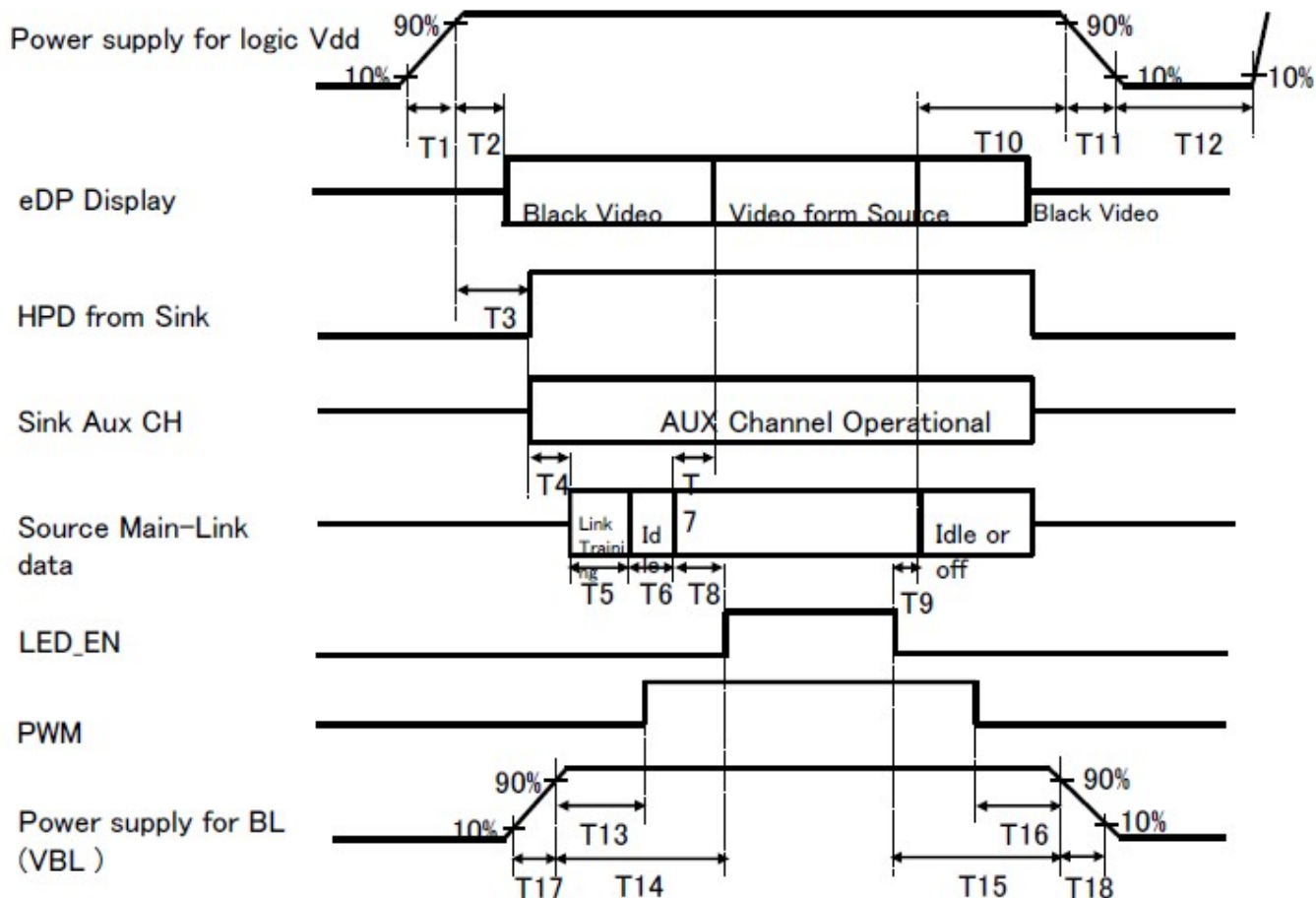
Timing Table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency@ Frame rate=60Hz	DCLK	101	152.6	158	MHz
Horizontal display area	thd	1920			PCLK
1 Horizontal Line	th	2080	2230	2400	PCLK
Vertical display area	tvd	1080			LINE
VSYNC period time	tv	1100	1140	1200	LINE



9、POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- | | |
|--|--|
| ● $0.5\text{ms} \leq T1 \leq 10\text{ ms}$ | ● $0\text{ms} \leq T7 \leq 50\text{ms}$ |
| ● $0\text{ms} \leq T2 \leq 200\text{ ms}$ | ● $0\text{ms} \leq T10 \leq 500\text{ ms}$ |
| ● $0\text{ms} \leq T3 \leq 200\text{ ms}$ | ● $0\text{ ms} \leq T11 \leq 10\text{ ms}$ |
| ● $0\text{ms} \leq T13$ | ● $500\text{ms} \leq T12$ |
| ● $0\text{ms} \leq T14$ | ● $0\text{ms} \leq T15$ |
| ● $0\text{ms} \leq T17$ | ● $0\text{ms} \leq T16$ |
| ● $T3+T4+T5+T6+T8 > 200\text{ms}$ | ● $0\text{ms} \leq T18$ |
| ● $T9 > 0\text{ms}$ | |

Notes:

1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.



10、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	60°C/120 hours
2	Low Temperature Storage	-20°C/120 hours
3	High Temperature Operating	50°C/120 hours
4	Low Temperature Operating	-10°C/120 hours
5	Temperature Cycle Storage	-10°C(30min.)~25(5min.)~50°C(30min.)×10cycles

A、Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- Air bubble in the LCD;
- Sealleak;
- Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B、Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11、INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

11.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC

Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

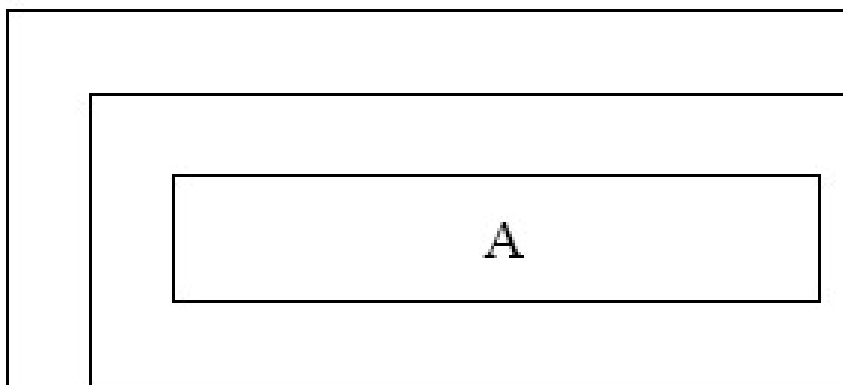
Minor defect: AQL 1.5

11.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45 °against perpendicular line. (Normal temperature 20~25 °C and normal humidity 60 ±15%RH)

11.3 Definition of Inspection Item.

A、 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B、 Definition of some visual defect

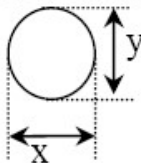
<i>Bright dot</i>	<i>Because of losing all or part function, bad pixel dots appear bright and the size is more than 50% of one dot in which LCD panel is displaying under black pattern.</i>
<i>Dark dot</i>	<i>Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.</i>

11.4 Major Defect

<i>Item No.</i>	<i>Items to be inspected</i>	<i>Inspection standard</i>	<i>Classification of defects</i>
<i>1</i>	<i>Functional defects</i>	<i>1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting</i>	<i>major</i>
<i>2</i>	<i>Missing</i>	<i>Missing component</i>	
<i>3</i>	<i>Outline dimension</i>	<i>Overall outline dimension beyond the drawing is not allowed</i>	

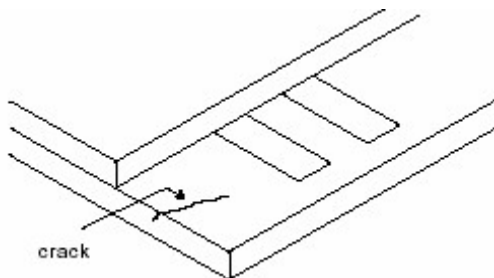
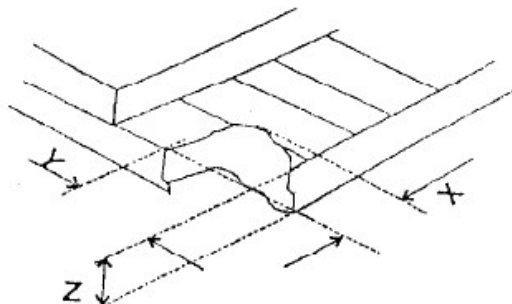
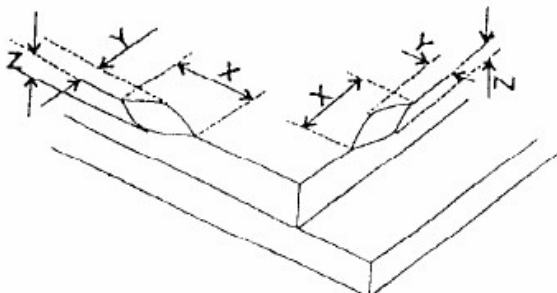


11.5、Minor Defect

Item No.	Items to be inspected	Inspection standard					Classification of defects
1	Bright dot /dark dot defect	Zone		Acceptable Qty			C
				A+B			
				4.3 ~7"	7 ~10.1"	>10.1"	
		Bright pixel dot		1	2	3	Acceptable
		Dark pixel dot		4	4	4	
		2bright dots adjacent		0	0	0	
		2dark dots adjacent		0	0	0	
		Total bright and dark dots		5	6	7	
Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.							
2	Dot defect  Φ=(x+y) /2	Zone		Acceptable Qty			C
				A+B			
				4.3" ~7"	7 ~10.1"	>10.1"	
		Size(mm) Φ ≤0.2		Acceptable	Acceptable	Acceptable	Acceptable
		0.2 < Φ ≤0.5		4	5	6	
		Φ >0.5		0	0	0	
		Note: 1. Minimum distance between defective dots is more than 5 mm; 2. The quantity of defect is zero in operating condition.					
		3	Linear defect	Zone		Acceptable Qty	
A+B							
Length	Width					4.3" ~7"	7 ~10.1"
Ignore	W ≤0.05			Acceptable	Acceptable	Acceptable	Acceptable
L ≤5.0	0.05 < W ≤0.1			4	5	6	
L >5.0	W >0.1			0	0	0	

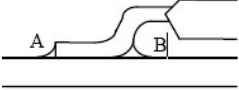
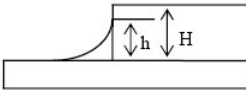


4	Polarizer defect	5.4.1 Polarizer Position (i) Shifting in position should not exceed the glass outline dimension. (ii) Incomplete covering of the viewing area due to shifting is not allowed. 5.4.2 Dirt on polarizer Dirt which can be wiped easily should be acceptable. 5.4.3 Polarizer Dent & Air bubble	<table><tr><th colspan="2" rowspan="3">Zone Size(mm)</th><th colspan="3">Acceptable Qty</th><th rowspan="3">C</th></tr><tr><th colspan="3">A+B</th></tr><tr><th>4.3''~7''</th><th>7~10.1''</th><th>>10.1''</th></tr><tr><td colspan="2">$\Phi \leq 0.2$</td><td>Acceptable</td><td>Acceptable</td><td>Acceptable</td><td rowspan="3">Acceptable</td></tr><tr><td colspan="2">$0.2 < \Phi \leq 0.5$</td><td>4</td><td>5</td><td>6</td></tr><tr><td colspan="2">$\Phi > 0.5$</td><td>0</td><td>0</td><td>0</td></tr></table>					Zone Size(mm)		Acceptable Qty			C	A+B			4.3''~7''	7~10.1''	>10.1''	$\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable	Acceptable	$0.2 < \Phi \leq 0.5$		4	5	6	$\Phi > 0.5$		0	0	0	Minor						
		Zone Size(mm)		Acceptable Qty			C																																			
				A+B																																						
				4.3''~7''	7~10.1''	>10.1''																																				
		$\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable	Acceptable																																			
		$0.2 < \Phi \leq 0.5$		4	5	6																																				
		$\Phi > 0.5$		0	0	0																																				
		5.4.4 Polarizer scratch (i) If the polarizer scratch can be seen after cover assembling or in the operating condition, judge by the linear defect of 5.3. (ii) If the polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following:	<table><tr><th colspan="2" rowspan="3">Zone Size (mm)</th><th colspan="3">Acceptable Qty</th><th rowspan="3">C</th></tr><tr><th colspan="3">A+B</th></tr><tr><th>Length</th><th>Width</th><th></th></tr><tr><td></td><td></td><td>4.3''~7''</td><td>7~10.1''</td><td>>10.1''</td><td></td></tr><tr><td>Ignore</td><td>$W \leq 0.05$</td><td>Acceptable</td><td>Acceptable</td><td>Acceptable</td><td rowspan="3">Acceptable</td></tr><tr><td>$1.0 < L \leq 5.0$</td><td>$0.05 < W \leq 0.20$</td><td>4</td><td>5</td><td>6</td></tr><tr><td>$L > 5.0$</td><td>$W > 0.2$</td><td>0</td><td>0</td><td>0</td></tr></table>					Zone Size (mm)		Acceptable Qty			C	A+B			Length	Width				4.3''~7''	7~10.1''	>10.1''		Ignore	$W \leq 0.05$	Acceptable	Acceptable	Acceptable	Acceptable	$1.0 < L \leq 5.0$	$0.05 < W \leq 0.20$	4	5		6	$L > 5.0$	$W > 0.2$	0	0	0
		Zone Size (mm)		Acceptable Qty			C																																			
				A+B																																						
Length	Width																																									
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$1.0 < L \leq 5.0$	$0.05 < W \leq 0.20$	4	5	6																																						
$L > 5.0$	$W > 0.2$	0	0	0																																						
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.					Minor																																			
	White/Black dot (MURA)	Visible under: ND3%; $D \leq 0.15mm$, Acceptable; $0.15mm < D \leq 0.5mm$, $N \leq 4$; $D > 0.5mm$, Not allowable.																																								

6	Glass defect	(i) Crack Cracks are not allowed. 	Minor								
		(ii) TFT chips on corner  <table><tr><td>X</td><td>Y</td><td>Z</td><td>Acceptable</td></tr><tr><td>≤3.0</td><td>≤3.0</td><td>Not more than the thickness of glass</td><td>N ≤3</td></tr></table> Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.	X	Y	Z	Acceptable	≤3.0	≤3.0	Not more than the thickness of glass	N ≤3	Minor
		X	Y	Z	Acceptable						
≤3.0	≤3.0	Not more than the thickness of glass	N ≤3								
(iii) Usual surface crack  <table><tr><td>X</td><td>Y</td><td>Z</td><td>Acceptable</td></tr><tr><td>≤1.5</td><td>≤1.5</td><td>Not more than the thickness of glass</td><td>N ≤4</td></tr></table> It is only applicable to the upper glass of LCD.	X	Y	Z	Acceptable	≤1.5	≤1.5	Not more than the thickness of glass	N ≤4	Minor		
X	Y	Z	Acceptable								
≤1.5	≤1.5	Not more than the thickness of glass	N ≤4								



11.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil ($\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed $\Phi 0.2$ mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
11	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B) of the lead to be covered by "Filet". Lead form to be assume over Solder. 	Minor
	3. Chips	$(3/2) H \geq h \geq (1/2) H$ 	Minor
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13$ mm. The diameter of solder ball $d \leq 0.15$ mm.	Minor
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm ² .	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major