

Surenoo HDMI Display Module Series

Model No.: SHD101D-1024600 USER MANUAL

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Reference Links

Surenoo HDMI Display Module Selection Guide

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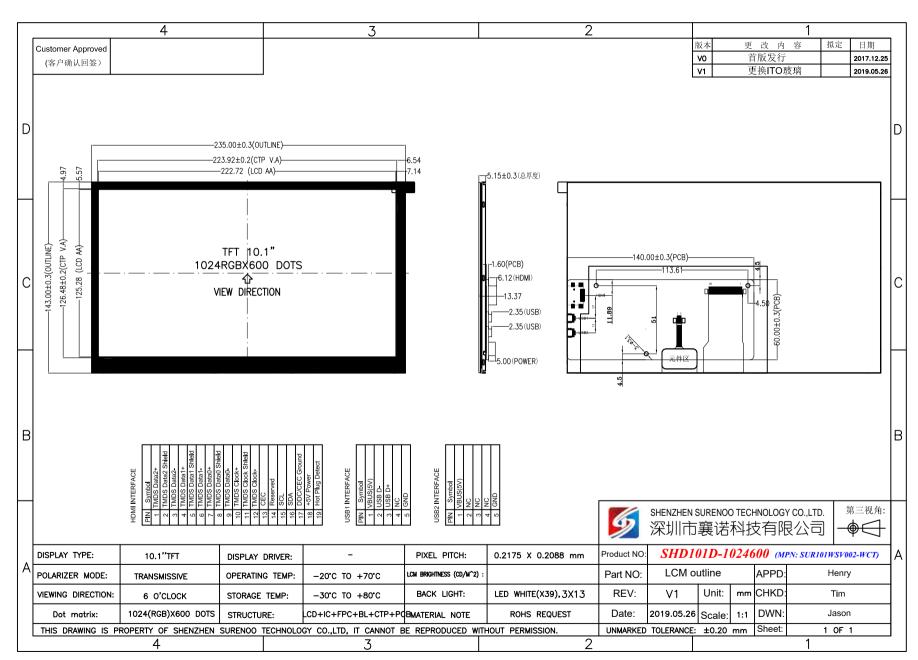
1, GENERAL INFORMATION

Item of general information		Contents	Unit	
LCD Display Size (Diagonal)		10.1	inch	
Module Structure	LCD Dis	splay + CTP Touch + PCB	-	
LCD Display Type	T	TFT/TRANSMISSIVE		
LCD Display Mode		Normally White	-	
Recommended Viewing Direction		6 O'clock	-	
Gray inversion Direction		12 O'clock	-	
Module size $(W \times H \times T)$	235.00×143.00×5.15		mm	
Active area (W×H)		mm		
Number of pixels (Resolution)		1024RGB×600	pixel	
Pixel pitch (W×H)		0.2175×0.2088	mm	
Color Pixel Arrangement		RGB Stripe	-	
M 1 1 I G T	LCD	HDMI Interface	-	
Module Interface Type	CTP Micro USB interface		-	
	Win7/W	Win7/Win8/Win10(Plug and play)		
System Support	Android/L	inux (need to be configured)	-	
Power Supply		USB	-	
Color Numbers		16.7M	-	
Backlight Type		White LED	-	

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2. EXTERNAL DIMENSIONS



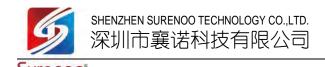
3. ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Тор	-20	70	$^{\circ}\!C$
Storage temperature	Tst	-30	80	$^{\circ}\!C$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4, ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Тур.	Max.	Unit
Power supply Voltage	USB1&USB2	-	5.0	-	V
Daniel La Comment	USB1	-	300	-	mA
Power supply Current	USB2	-	450	-	mA



5, CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1024×600	pixel	-
Surface Hardness	6Н	-	-
Transparency	≥86%	-	-
Driver IC	GT9271	-	-
Interface Type	USB	-	-
Support Points	10 (Max)	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

FIG 2.

6 LECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0.0	-	20	40	ms	FIG 1.	4
Contrast I	Ratio	CR	$\theta=0$ $\varnothing=0$	-	350	-	-	FIG 2.	1
Luminance un	iformity	<i>δWHITE</i>	Ta=25°C	-	80	-	%	FIG 2.	3
Surface Lum	inance	Lv	10. 20 0	_	300	-	cd/m2	FIG 2.	2
CIE (x, y)		White x	$\theta = 0$	0.273	0.313	0.353			
chromaticity	White	White y	<i>Ø</i> =0 <i>Ta</i> =25°C	0.289	0.329	0.369	-	FIG 2.	5
	Ø=90(1	2 o'clock)		50	60	_	deg	FIG 3.	
Viewing	Ø=270((6 o'clock)	CD > 10	50	60	_	deg		6
angle range	Ø=0(3	o'clock)	<i>CR</i> ≥ 10	60	70	-	deg		
	Ø=180(9 o'clock)		60	70	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Note 3. The uniformity in surface luminance (SWHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

 $\delta \text{WHITE} = \frac{\textit{Minimum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{\textit{Maximum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 4. The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For more information see FIG 1.

Note 5. CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

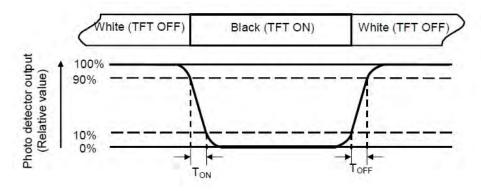


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

CIE(x, y) chromaticity

A: H/6; B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

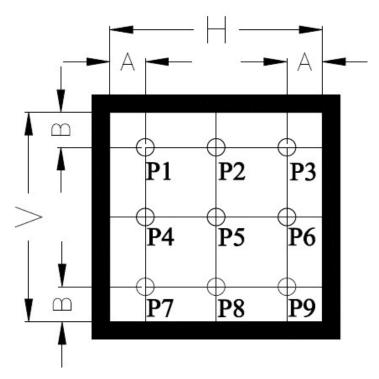
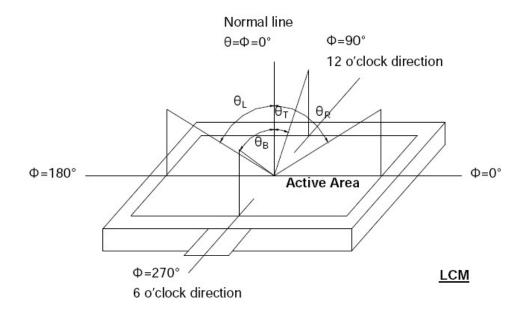
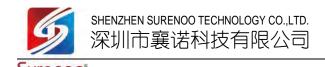


FIG.3. The definition of viewing angle





7. INTERFACE DESCRIPTION

A. HDMI Interface Description

<i>NO</i> .	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect



B . USB1 Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power(5V)
2	D-	USB Data-
3	D+	USB Data+
4	NC	No connection
5	GND	Power Ground

C. USB2 Interface Description

NO.	Symbol	DESCRIPTION
1	VUSB	USB Power(5V)
2~4	NC	No connection
5	GND	Power Ground

8 RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- Missing segments;
- ➤ Glass crack;
- Current is twice higher than initial value.

B, Remark:

- The test samples should be applied to only one test item.
- > Sample size for each test item is $5\sim10$ pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

9. INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

9.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

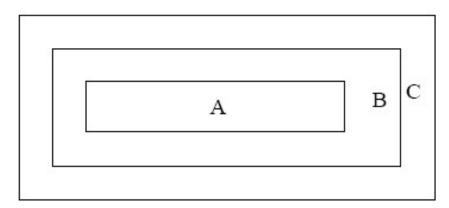
Minor defect: AQL 1.5

9.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of $20\sim40W$ light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature $20\sim25$ ° Cand normal humidity 60 $\pm15\%RH$)

9.3 Definition of Inspection Item.

A Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone $A + Zone B = minimum \ Viewing \ area)$

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig. 1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B. Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the				
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under				
	black pattern.				
D. I. I.	Dots appear dark and unchanged in size in which LCD panel is displaying				
Dark dot	under pure red, green, blue picture, or pure whiter picture.				

9.4 Major Defect

Item No.	Items to be	Inspection standard	Classification of defects		
I	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major		
2	Missing	Missing component			
3	3 Outline dimension Overall outline dimension beyond the drawing is not allowed				

9.5. Minor Defect

Item No.	Items to be		Inspection standard							
			Zone			Accepto	able Qty			
						A+B				
					4.3" ~	7~10.1	>10.1"	С		
		Brig	ght pixel do	t	1	2	3			
	Bright dot	Da	ırk pixel dot		4	4	4	A		
1	/dark dot	2bright	t dots adja	cent	0	0	0	Acceptable	Minor	
	defect	2dark	dots adjac	cent	0	0	0	otab		
		Total b	right and dots	dark	5	6	7	le		
		Pixel dots	' function i	is normal,	but brig	ht dots ca	is more than . used by foreig t defect of 5.2	gn		
			Zone		A	cceptable	Qty			
				A+B						
		Size(mm)	Size(mm)		4.3"~7" 7		>10.1"	С		
	Dot defect	ot defect $\phi \leqslant 0.2$ $0.2 < \phi \leqslant 0.5$		Accepta	ble Ac	ceptable	Acceptable	Асс		
2	$\left(\begin{array}{c} \end{array}\right)$			$0.2 < \Phi \leqslant 0.5$ 4 5 6		Acceptable	Minor			
	$\stackrel{\smile}{\longleftrightarrow}$	Φ>0.5		0		0	0	ble		
	$\Phi = (x+y)/2$	Note: 1. Minimus 2. The qua			-		nore than 5 mi	n;		
			Zone		A	cceptable	Qty			
		Size (mm)			A+B					
	Linear	Length	Width	4.3"~7	" 7	~10.1"	>10.1"	с	16	
3	defect	Ignore	<i>W</i> ≤0.05	Acceptal	ble Ac	ceptable	Acceptable	Ac	Minor	
		L ≤5.0	0.05 < W≤0.1	4		5	6	Acceptable		
		L>5.0	W>0.1	0		0	0	le		

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		5.4.1 Polarizer Position (i) Shifting in position should not exceed the glass outline dimension. (ii) Incomplete covering of the viewing area due to shifting is not allowed. 5.4.2 Dirt on polarizer Dirt which can be wiped easily should be acceptable. 5.4.3 Polarizer Dent & Air bubble Zone Acceptable Qty $A+B$ Size(mm) $A: v \sim 7$ $v \sim 10.1$						
			Φ≤0.5	4	5	6	Acceptable	
4	Polarizer defect	Φ	>0.5	0	0	0	le	Minor
		(i) If the por in the (ii) If the	operating polarizer or some Zone	scratch can be condition, just scratch can special angle 4.3"~7" Acceptable 4	idge by the li be seen only	inear defect of in non-oper e following:	of 5.3.	
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.						
	White/Black dot (MURA)	Visible under: ND3%; $D \le 0.15$ mm, Acceptable; 0.15 mm $< D \le 0.5$ mm, $N \le 4$; $D>0.5$ mm, Not allowable.				Minor		

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	Glass defect	(i) Crack Cracks are not allowed.	Minor
6		(ii) TFT chips on corner X Y Z $Acceptable$ $1 \le 3.0$ $1 \le 3.0$ $1 \le 3.0$ $2 \le 3.0$ $3 \le 3.0$ $4 \le 3.0$ $5 \ge 3.0$ $5 \ge$	Minor
		(iii) Usual surface crack X Y Z $Acceptable$	Minor

9.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects	
1	Difference in Spec.	Not allowable	<u> </u>	
2	Pattern peeling	No substrate pattern peeling and floating	Major	
3		No soldering missing	Major	
	Soldering defects	No soldering bridge	Major	
		No cold soldering	Minor	
1	Resist flaw on PCB	Visible copper foil (Φ 0.5 mm or more) on substrate	Minor	
4		pattern is not allowed		
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major	
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor	
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor	
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ 0.2mm)	Minor	
9	Stain	No stain to spoil cosmetic badly	Minor	
10	Plate discoloring	No plate fading, rusting and discoloring	Minor	
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor	
11		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor	
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor	
	3. Chips	(3/2) H ≥h ≥(1/2) H \$\int_h \hat{\hat{\pm}}\$H	Minor	
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor	
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor	
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major	