



SURENOO GRAPHIC OLED SERIES DISPLAY

Product Specification

Part Name: OEL Display Module
SOG12864E_M096

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Graphic OLED Display Selection Guide

Graphic OLED Module

Graphic OLED Panel

SSD1306

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1. Basic Specifications

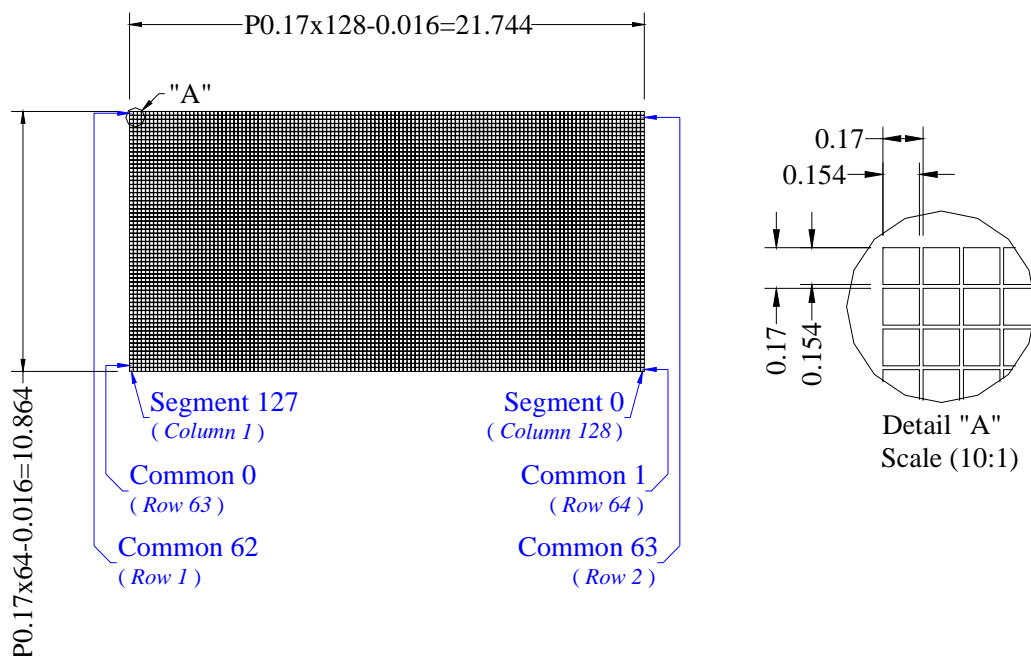
1.1 Display Specifications

- 1) Display Mode: Passive Matrix
- 2) Display Color: Monochrome (White)
- 3) Drive Duty: 1/64 Duty

1.2 Mechanical Specifications

- 1) Outline Drawing: According to the annexed outline drawing
- 2) Number of Pixels: 128×64
- 3) PCB Size: $27.3 \times 27.8 \times 2.65$ (mm)
- 4) Active Area: 21.744×10.864 (mm)
- 5) Pixel Pitch: 0.17×0.17 (mm)
- 6) Pixel Size: 0.154×0.154 (mm)

1.3 Active Area / Memory Mapping & Pixel Construction

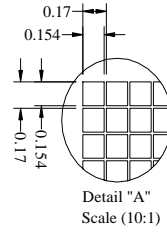
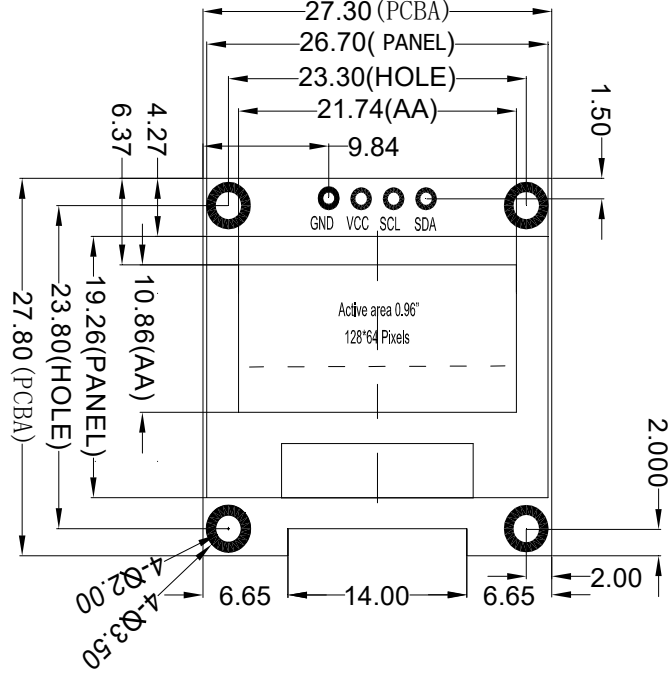
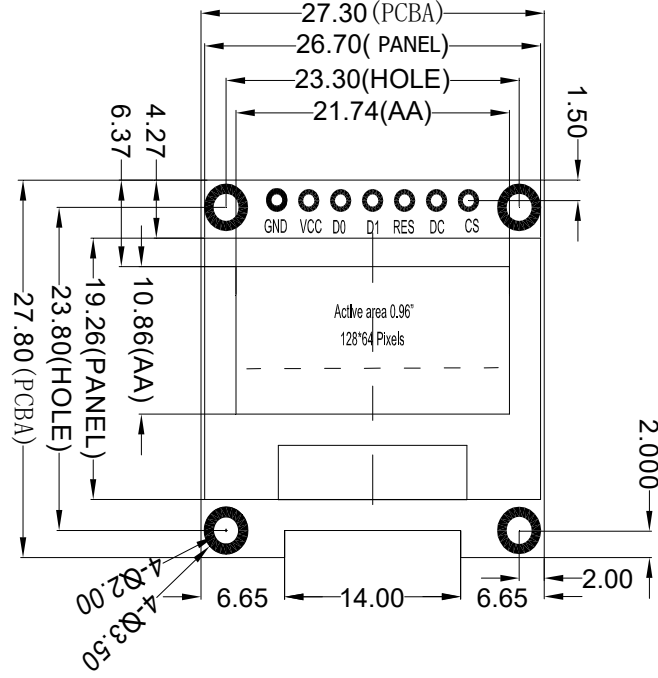




1.4 Mechanical Drawing


NOTES:

- 1.DISPLAY TYPE: 0.96" 128*64 OLED
2. VIEWING DIRECTION: ALL
3. POLARIZER MODE: TRANSMISSIVE/NORMALLY BLACK
4. DRIVER IC: SSD1306
5. Resolution: 128x64
- 6.INTERFACE: SPI / IIC (7P); IIC (4P)
7. VOLTAGE: 3.3V
8. OPERATING TEMP: -40°C~70°C



| Item | Date | Remark |
|------|----------|------------------|
| A | 20190425 | Original Drawing |

| Pin Descriptions | | |
|------------------|---------|-----|
| Pin | 7P | 5P |
| 1 | GND | GND |
| 2 | VCC | VCC |
| 3 | D0(SCL) | SCL |
| 4 | D1(SDA) | SDA |
| 5 | RES | |
| 6 | DC | |
| 7 | CS | |

| | | | | | | | | | | | | | | |
|--------------------------------|--|---|--|-------|--|----------------|-------|------|-----------------------|--|------------|--------------------------------|-----------------------------------|------------------------------|
| Customer Approval Signature | |  SHENZHEN SURENOO TECHNOLOGY CO.,LTD. 深圳市襄诺科技有限公司 | | | | Drawing Number | | Rev. | | | | | | |
| | | Unless Otherwise Specified | | Title | SOG212864E_M096 PM-OLED Display Module | | | | Material | | | | | |
| | | Unit mm | | | Pixel Number: 128x64, Monochrome | | | | Soda Lime / Polyimide | | | | | |
| | | General Roughness  | | | | | | | | | | | | |
| | | Tolerance | | | | | | | | | | | | |
| | | Dimension ±0.3 | | | By HBK | | Drawn | | E.E. | | Panel / E. | | P.M. | |
| Angle ±1 | | Date 20190425 | | | | | | | | | | Scale 1:1 | Sheet 1 of 1 | Size A4 |

1.5 Pin Definition

Model No.: SOG12864E1_M096

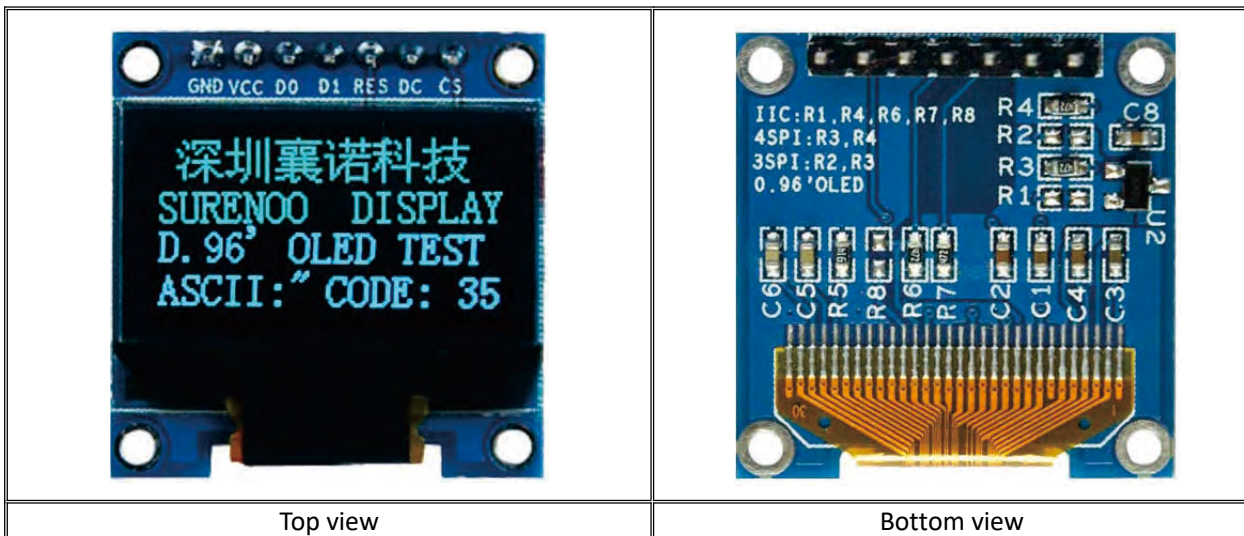
| PIN No. | Symbol | Description |
|---------|--------|--|
| 1 | GND | Ground of Logic Circuit This is a ground pin. It must be connected to ground |
| 2 | VCC | Power Supply for OLED This is a voltage supply pin. It must be connected to source |
| 3 | SCL | The serial clock input SCL |
| 4 | SDA | The serial data input SDA |
| 5 | RES | Power Reset for Controller and Driver This pin is reset signal input. When the pin is low, initialization of the chip is executed. Keep this pin pull high during normal operation |
| 6 | DC | Data/Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at SDA is treated as display data. When the pin is pulled low, the input at SDA will be transferred to the command register. |
| 7 | CS | Chip Select This pin is the chip select pin. Low enable, high disable. |

Model No.: SOG12864E2_M096

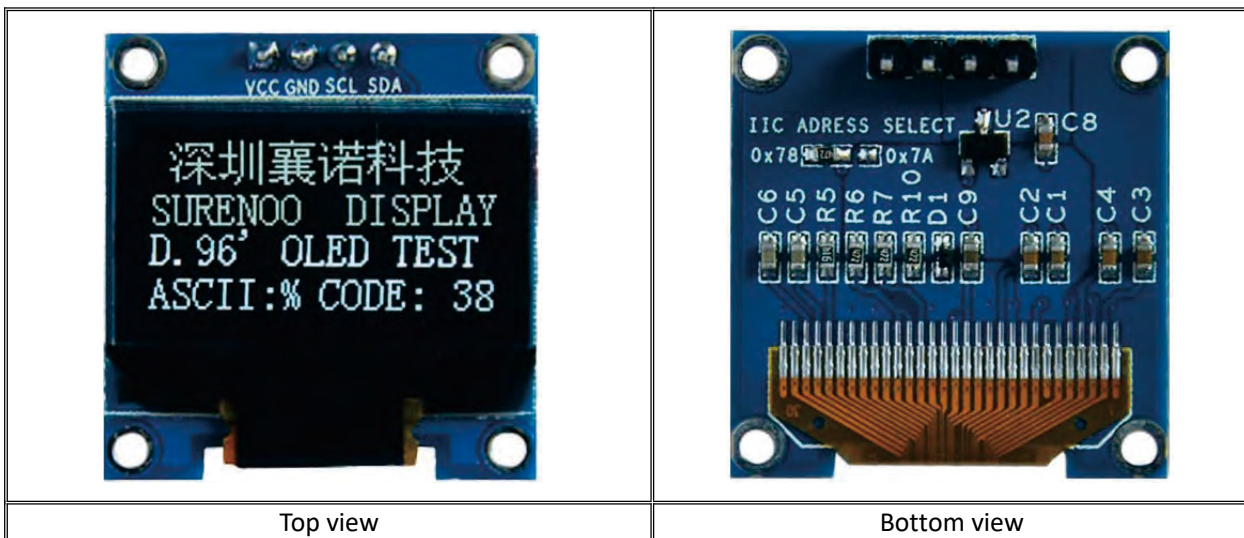
| PIN No. | Symbol | Description |
|---------|--------|--|
| 1 | GND | Ground of Logic Circuit This is a ground pin. It must be connected to ground |
| 2 | VCC | Power Supply for OLED This is a voltage supply pin. It must be connected to source |
| 3 | SCL | The serial clock input SCL |
| 4 | SDA | The serial data input SDA |

1.6 Product Picture

Model No.: SOG12864E1_M096



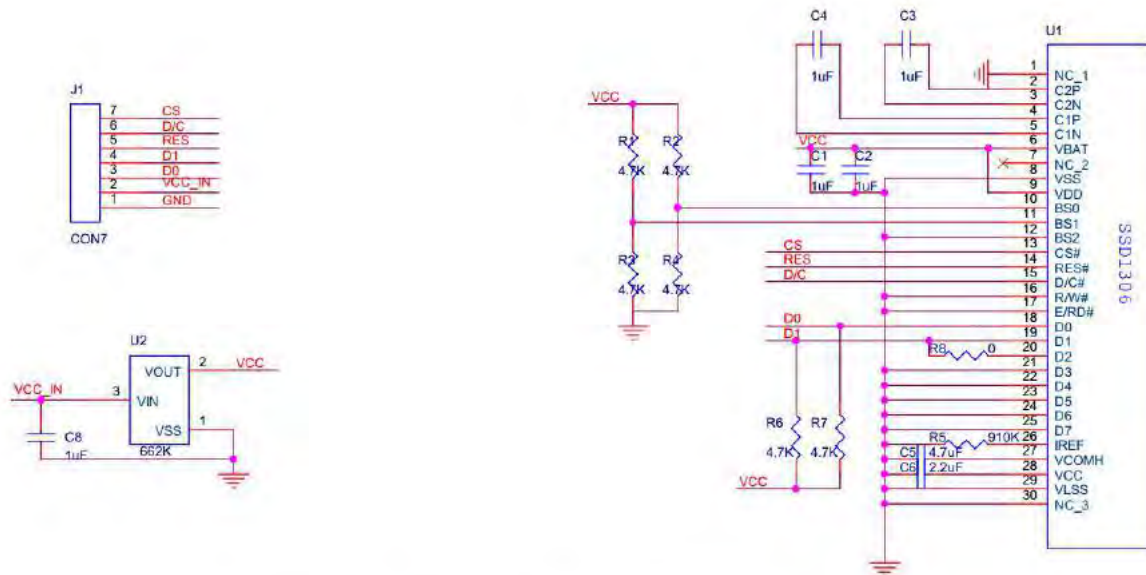
Model No.: SOG12864E2_M096





1.7 Schematic Diagram

Model No.: SOG12864E1_M096

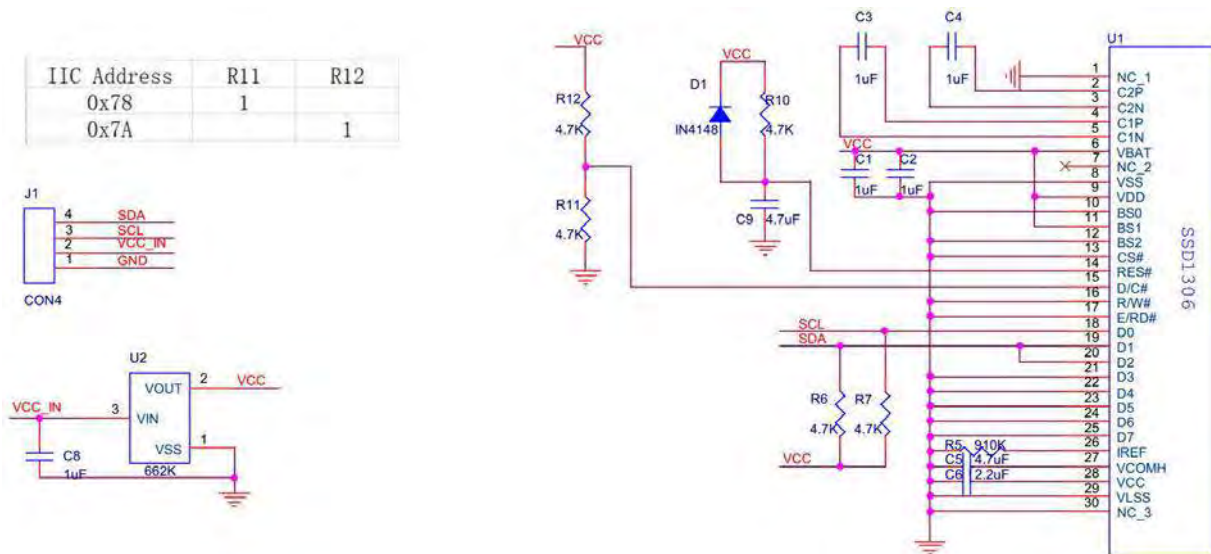


| Driving mode | R1 | R2 | R3 | R4 | R6 | R7 | R8 |
|--------------|----|----|----|----|----|----|----|
| 4-wire SPI | | | 1 | 1 | | | |
| 3-wire SPI | | 1 | 1 | | | | |
| IIC | 1 | | | 1 | 1 | 1 | 1 |

SPI Switch to IIC :

1. Change R3 to R1.
2. Ground DC pin and CS pin.
3. Use IIC program.

Model No.: SOG12864E2_M096



The I2C communication interface consists of slave address bit DC, I2C-bus data signal SDA (SDAOUT/D2 for output and SDAIN/D1 for input) and I2C-bus clock signal SCL (D0). Both the data and clock signals must be connected to pull-up resistors.

a) Slave address bit (SA0) has to recognize the slave address before transmitting or receiving any information by the I2C-bus. The device will respond to the slave address following by the slave address bit ("SA0" bit) and the read/write select bit ("R/W#" bit) with the following byte format,

| b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 |
|----|----|----|----|----|----|-----|------|
| 0 | 1 | 1 | 1 | 1 | 0 | SA0 | R/W# |

b) "SA0" bit provides an extension bit for the slave address. Either "0111100" or "0111101", can be selected as the slave address of OLED. **D/C# pin acts as SA0 for slave address selection.** "R/W#" bit is used to determine the operation mode of the I2C-bus interface. R/W#=1, it is in read mode. R/W#=0, it is in write mode

| | | | |
|------|----------------------------|------|--|
| 0x3C | 0 1 1 1 1 0 0 0 (=0x78) | DC=0 | The default |
| 0x3D | 0 1 1 1 1 0 1 0 (=0x7A) | DC=1 | The user needs to switch the resistance position to modify the IIC address |

2. Absolute Maximum Ratings

| Parameter | Symbol | Min | Max | Unit | Notes |
|------------------------------------|-------------------|--------|-----|------|-------|
| Supply Voltage for Display | VCC | 3 | 5 | V | 1, 2 |
| Supply Voltage for Logic | SCL/SDA/RES/DC/CS | 1.65 | 3.3 | V | |
| Operating Temperature | T _{OP} | -40 | 80 | °C | |
| Storage Temperature | T _{STG} | -40 | 85 | °C | 3 |
| Life Time (120 cd/m ²) | | 10,000 | - | hour | 4 |
| Life Time (80 cd/m ²) | | 30,000 | - | hour | 4 |
| Life Time (60 cd/m ²) | | 50,000 | - | hour | 4 |

Note 1: All the above voltages are on the basis of “GND = 0V”.

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 3. “Optics & Electrical Characteristics”. If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

Note 3: The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80°C.

Note 4: VCC = 3.3 V, T_a = 25°C, 50% Checkerboard.

Software configuration follows Section 4.4 Initialization.

End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

3.Optics & Electrical Characteristics

3.1 Optics Characteristics

| Characteristics | Symbol | Conditions | Min | Typ | Max | Unit |
|--|------------|-------------|--------------|--------------|--------------|-------------------|
| Brightness (VCC Supplied Externally) | L_{br} | Note 5 | 150 | - | - | cd/m ² |
| Brightness (VCC Generated Internal DC/DC) | L_{br} | Note 6 | 150 | 180 | - | cd/m ² |
| C.I.E. | (x) (y) | C.I.E. 1931 | 0.28 0.31 | 0.32 0.35 | 0.36 0.39 | |
| Dark Room Contrast | CR | | - | 2000:1 | - | |
| Viewing Angle | | | - | Free | - | degree |

* Optical measurement taken at VDD = 2.8V, VCC = 7.25V.

Software configuration follows Section 4.2 Initialization.

3.2 DC Characteristics

| Characteristics | Symbol | Conditions | Min | Typ | Max | Unit |
|---|-----------|------------------------------------|---------------------|------|---------------------|------|
| Supply Voltage for Logic | V_{DD} | | 1.65 | 2.8 | 3.3 | V |
| Supply Voltage for Display (Supplied Externally) | V_{CC} | Note 5 (Internal DC/DC Disable) | 11.5 | 12 | 12.5 | V |
| Supply Voltage for DC/DC | V_{BAT} | Internal DC/DC Enable | 3.5 | - | 4.2 | V |
| Supply Voltage for Display (Generated by Internal DC/DC) | V_{CC} | Note 6 (Internal DC/DC Enable) | 7.0 | - | 7.5 | V |
| High Level Input | V_{IH} | $I_{OUT} = 100\mu A, 3.3MHz$ | $0.8 \times V_{DD}$ | - | V_{DD} | V |
| Low Level Input | V_{IL} | $I_{OUT} = 100\mu A, 3.3MHz$ | 0 | - | $0.2 \times V_{DD}$ | V |
| High Level Output | V_{OH} | $I_{OUT} = 100\mu A, 3.3MHz$ | $0.9 \times V_{DD}$ | - | V_{DD} | V |
| Low Level Output | V_{OL} | $I_{OUT} = 100\mu A, 3.3MHz$ | 0 | - | $0.1 \times V_{DD}$ | V |
| Operating Current for VDD | I_{DD} | | - | 180 | 300 | uA |
| Operating Current for VCC (VCC Supplied Externally) | I_{CC} | Note 7 | - | 9 | 15 | mA |
| Operating Current for VBAT (VCC Generated by Internal | I_{BAT} | Note 8 | - | 25.6 | 32 | mA |

| DC/DC) | | | | | | |
|----------------------------|----------------|--|---|---|----|----|
| Sleep Mode Current for VDD | $I_{DD,SLEEP}$ | | - | 1 | 5 | uA |
| Sleep Mode Current for VCC | $I_{CC,SLEEP}$ | | - | 2 | 10 | uA |

Note 5 & 6: Brightness (Lbr) and Supply Voltage for Display (VPP) are subject to the change of the panel characteristics and the customer's request.

Note 7: VDD = 2.8V, VCC = 12V, 100% Display Area Turn on.

Note 8: VDD = 2.8V, VCC = 7.25V, 100% Display Area Turn on.

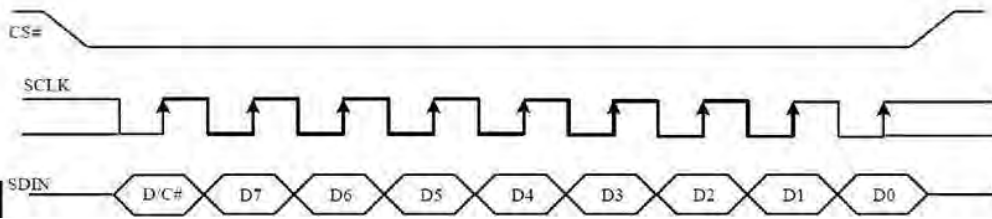
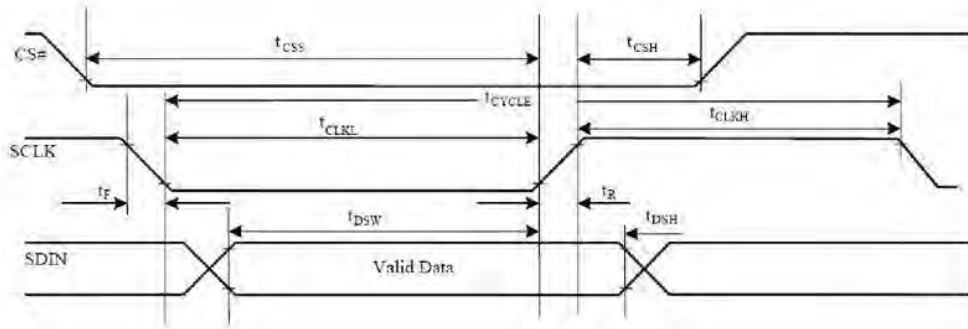
* Software configuration follows Section 4.2 Initialization.

3.3AC Characteristics

3.3.1 SPI Interface Timing Characteristics:

| Symbol | Description | Min | Max | Unit |
|-------------|------------------------|-----|-----|------|
| t_{cycle} | Clock Cycle Time | 100 | - | ns |
| t_{css} | Chip Select Setup Time | 20 | - | ns |
| t_{csh} | Chip Select Hold Time | 10 | - | ns |
| t_{dsw} | Write Data Setup Time | 15 | - | ns |
| t_{dhw} | Write Data Hold Time | 15 | - | ns |
| t_{CLKL} | Clock Low Time | 20 | - | ns |
| t_{CLKH} | Clock High Time | 20 | - | ns |
| t_R | Rise Time | - | 40 | ns |
| t_F | Fall Time | - | 40 | ns |

* ($V_{DD} - V_{SS} = 1.65V$ to $3.3V$, $T_a = 25^\circ C$)



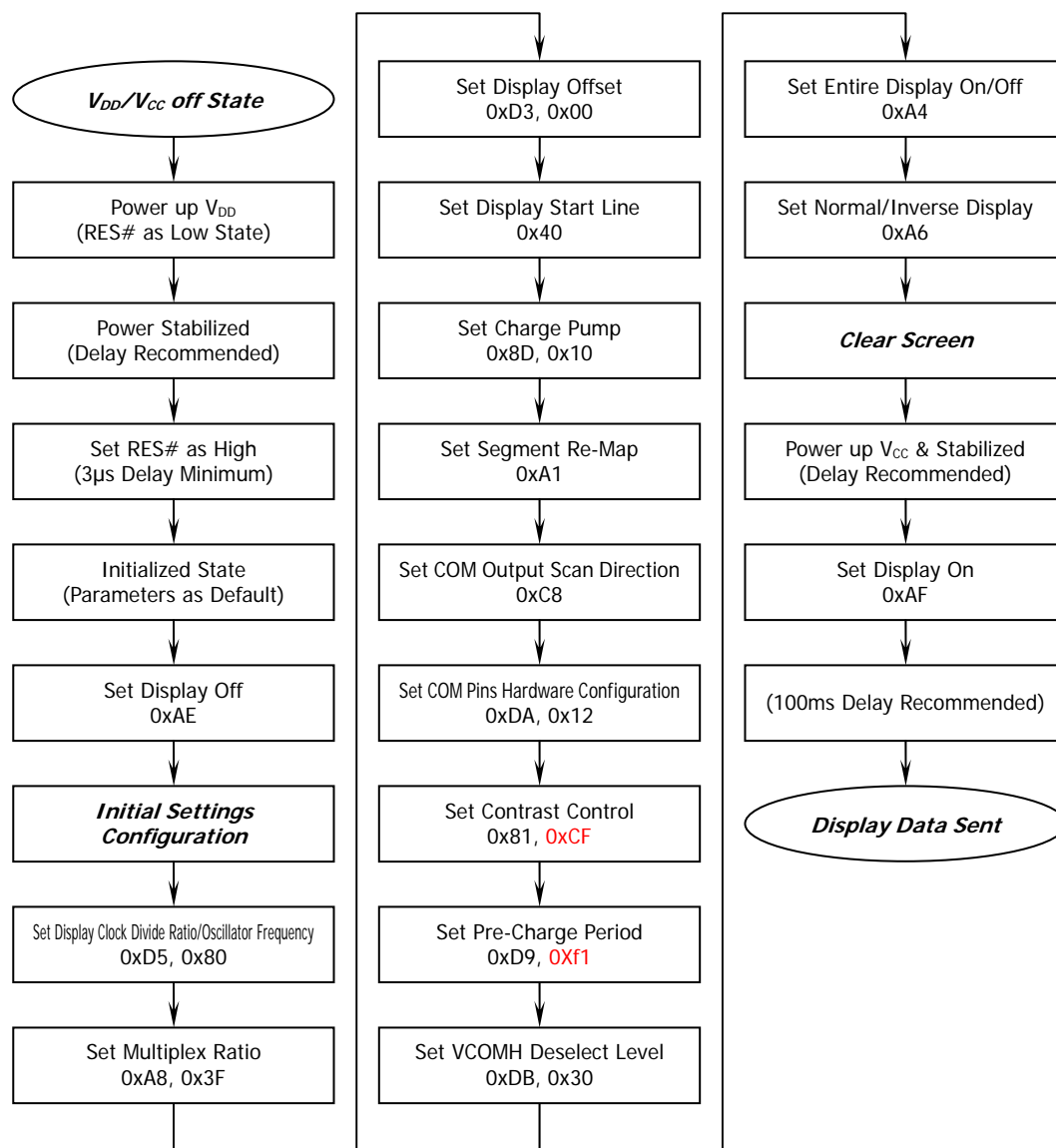
4. Functional Specification

4.1 Commands

| D/C | Hex | D | D | D | D | D3 | D2 | D | D0 | Command | Description | | | | | | | | | | | | |
|--------|------------------|----------------------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|--------------------------------------|---|--------|------------|----------------------------------|------|-----|--------------------------|------|-----|----------------------------------|------|-----|--------------------------|
| 00 | 81 A[7:0] | 1 A ₇ | 0 A ₆ | 0 A ₅ | 0 A ₄ | 0 A ₃ | 0 A ₂ | 0 A ₁ | 1 A ₀ | Set Contrast Control | Double byte command to select 1 out of 256 contrast steps. Contrast increases as the value increases. (RESET = 7Fh) | | | | | | | | | | | | |
| 00 | DB A[7:0] | 1 0 | 1 A ₆ | 0 A ₅ | 1 A ₄ | 1 0 | 0 0 | 1 0 | 1 0 | Set V _{COMH} Deselect Level | <table><tr><td>A[6:4]</td><td>Hex cod</td><td>V_{COMH} deselect level</td></tr><tr><td>000b</td><td>00h</td><td>~ 0.65 x V_{CC}</td></tr><tr><td>010b</td><td>20h</td><td>~ 0.77 x V_{CC} (RESET)</td></tr><tr><td>011b</td><td>30h</td><td>~ 0.83 x V_{CC}</td></tr></table> | A[6:4] | Hex cod | V _{COMH} deselect level | 000b | 00h | ~ 0.65 x V _{CC} | 010b | 20h | ~ 0.77 x V _{CC} (RESET) | 011b | 30h | ~ 0.83 x V _{CC} |
| A[6:4] | Hex cod | V _{COMH} deselect level | | | | | | | | | | | | | | | | | | | | | |
| 000b | 00h | ~ 0.65 x V _{CC} | | | | | | | | | | | | | | | | | | | | | |
| 010b | 20h | ~ 0.77 x V _{CC} (RESET) | | | | | | | | | | | | | | | | | | | | | |
| 011b | 30h | ~ 0.83 x V _{CC} | | | | | | | | | | | | | | | | | | | | | |
| 0 | A6/A7 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | X ₀ | Set Normal/Inverse Display | A6h, X[0]=0b: Normal display (RESET) 0 in RAM: OFF in display panel 1 in RAM: ON in display panel A7h, X[0]=1b: Inverse display 0 in RAM: ON in display panel | | | | | | | | | | | | |
| 0 | AE AF | 1 | 0 | 1 | 0 | 1 | 1 | 1 | X ₀ | Set Display ON/OFF | AEh, X[0]=0b:Display OFF (sleep mode) (RESET) AFh X[0]=1b:Display ON in normal mode | | | | | | | | | | | | |
| 0 | A0/A1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | X ₀ | Set Segment Re-map | A0h, X[0]=0b: column address 0 is mapped to SEG0 (RESET) A1h, X[0]=1b: column address 127 is mapped to SEG0 | | | | | | | | | | | | |
| 0 | C0/C8 | 1 | 1 | 0 | 0 | X ₃ | 0 | 0 | 0 | Set COM Output Scan Direction | C0h, X[3]=0b: normal mode (RESET) Scan from COM0 to COM[N –1] C8h, X[3]=1b: remapped mode. Scan from COM[N-1] to COM0 | | | | | | | | | | | | |

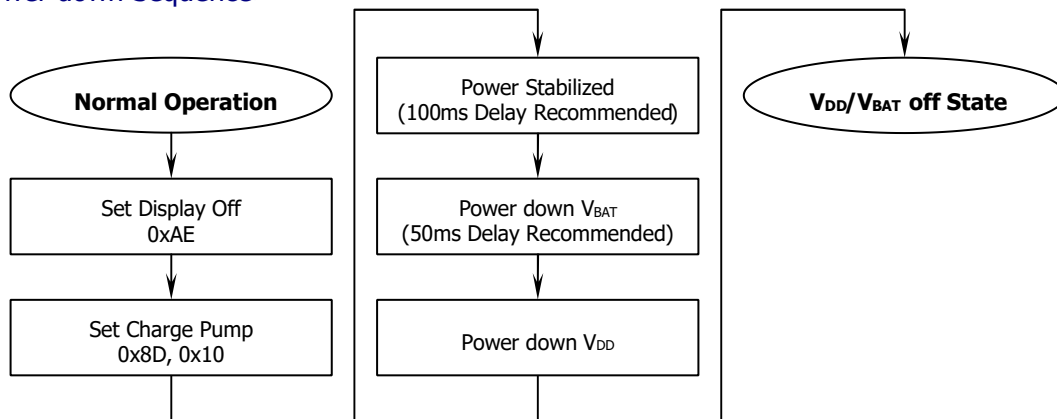
4.2VCC Generated by Internal DC/DC Circuit

<Power up Sequence>

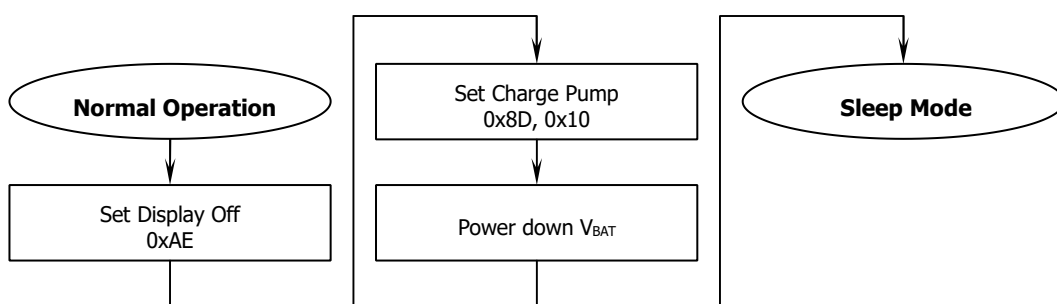


If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.

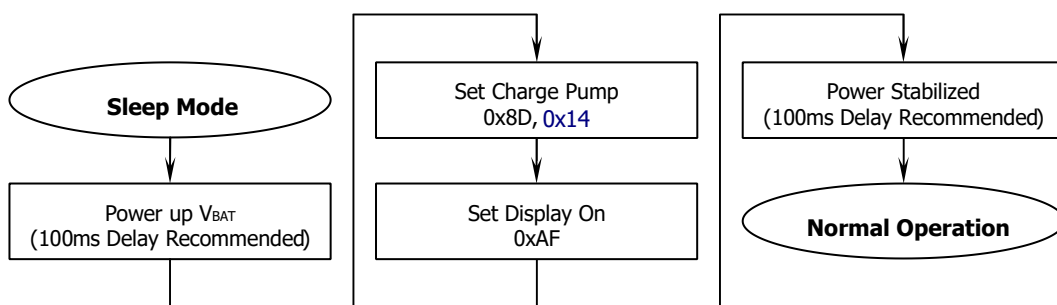
<Power down Sequence>



<Entering Sleep Mode>



<Exiting Sleep Mode>



```
void OLED_Init(void)
{
    //OLED 复位
    OLED_RES_Clr();//RES 置 0
    delay_ms(200);//延时 200ms
    OLED_RES_Set();//RES 置 1

    //OLED 初始化
    OLED_WR_Byte(0xAE,OLED_CMD);//--turn off oled panel
    OLED_WR_Byte(0x00,OLED_CMD);//--set low column address
    OLED_WR_Byte(0x10,OLED_CMD);//--set high column address
    OLED_WR_Byte(0x40,OLED_CMD);//--set start line address
    OLED_WR_Byte(0x81,OLED_CMD);//--set contrast control register
    OLED_WR_Byte(0xCF,OLED_CMD);// Set SEG Output Current Brightness
    OLED_WR_Byte(0xA1,OLED_CMD);//--Set SEG/Column Mapping      0xa0 左右反置
0xa1 正常
    OLED_WR_Byte(0xC8,OLED_CMD);//Set COM/Row Scan Direction      0xc0 上下反置
0xc8 正常
    OLED_WR_Byte(0xA6,OLED_CMD);//--set normal display
    OLED_WR_Byte(0xA8,OLED_CMD);//--set multiplex ratio(1 to 64)
    OLED_WR_Byte(0x3f,OLED_CMD);//--1/64 duty
    OLED_WR_Byte(0xD3,OLED_CMD);//--set display offset   Shift      Mapping      RAM
Counter (0x00~0x3F)
    OLED_WR_Byte(0x00,OLED_CMD);//-not offset
    OLED_WR_Byte(0xd5,OLED_CMD);//--set display clock divide ratio/oscillator
frequency
    OLED_WR_Byte(0x80,OLED_CMD);//--set divide ratio, Set Clock as 100 Frames/Sec
    OLED_WR_Byte(0xD9,OLED_CMD);//--set pre-charge period
    OLED_WR_Byte(0xF1,OLED_CMD);//Set Pre-Charge as 15 Clocks & Discharge as 1
Clock
    OLED_WR_Byte(0xDA,OLED_CMD);//--set com pins hardware configuration
    OLED_WR_Byte(0x12,OLED_CMD);
    OLED_WR_Byte(0xDB,OLED_CMD);//--set vcomh
    OLED_WR_Byte(0x30,OLED_CMD);//Set VCOM Deselect Level
    OLED_WR_Byte(0x20,OLED_CMD);//-Set Page Addressing Mode (0x00/0x01/0x02)
    OLED_WR_Byte(0x02,OLED_CMD);//
    OLED_WR_Byte(0x8D,OLED_CMD);//--set Charge Pump enable/disable
    OLED_WR_Byte(0x14,OLED_CMD);//--set(0x10) disable
    OLED_Clear();
```

```
OLED_WR_Byte(0xAF,OLED_CMD);  
}
```

```
#define OLED_CMD  0 //write command  
#define OLED_DATA 1 //write data
```

```
void OLED_WR_Byte(u8 dat,u8 cmd)  
{  
    u8 i;  
    if(cmd)  
        OLED_DC_Set();  
    else  
        OLED_DC_Clr();  
    OLED_CS_Clr();  
    for(i=0;i<8;i++)  
    {  
        OLED_SCL_Clr();  
        if(dat&0x80)  
            OLED_SDA_Set();  
        else  
            OLED_SDA_Clr();  
        OLED_SCL_Set();  
        dat<<=1;  
    }  
    OLED_CS_Set();  
    OLED_DC_Set();  
}
```

5. Reliability

5.1 Contents of Reliability Tests

| Item | Conditions | Criteria |
|-------------------------------------|--|---------------------------------|
| High Temperature Operation | 70°C, 240 hrs | The operational functions work. |
| Low Temperature Operation | -40°C, 240 hrs | |
| High Temperature Storage | 85°C, 240 hrs | |
| Low Temperature Storage | -40°C, 240 hrs | |
| High Temperature/Humidity Operation | 60°C, 90% RH, 120 hrs | |
| Thermal Shock | -40°C ⇔ 85°C, 24 cycles 60 mins dwell | |

* The samples used for the above tests do not include polarizer.

* No moisture condensation is observed during tests.

5.2 Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.

6. Outgoing Quality Control Specifications

6.1 Environment Required

Customer's test & measurement are required to be conducted under the following conditions:

| | |
|---|-------------|
| Temperature: | 23 ± 5°C |
| Humidity: | 55 ± 15% RH |
| Fluorescent Lamp: | 30W |
| Distance between the Panel & Lamp: | ≥ 50cm |
| Distance between the Panel & Eyes of the Inspector: | ≥ 30cm |
| Finger glove (or finger cover) must be worn by the inspector. | |
| Inspection table or jig must be anti-electrostatic. | |

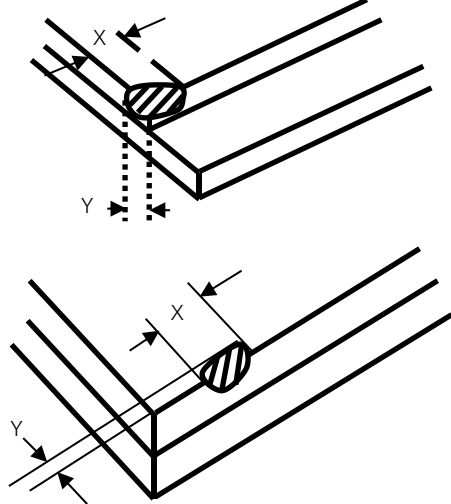
6.2 Sampling Plan

Level II, Normal Inspection, Single Sampling, MIL-STD-105E

6.3 Criteria & Acceptable Quality Level

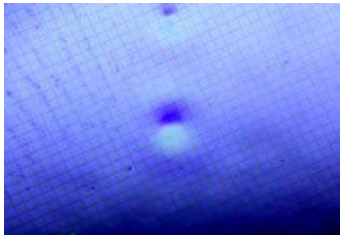
| Partition | AQL | Definition |
|-----------|------|---|
| Major | 0.65 | Defects in Pattern Check (Display On) |
| Minor | 1.0 | Defects in Cosmetic Check (Display Off) |

6.3.1 Cosmetic Check (Display Off) in Non-Active Area

| Check Item | Classification | Criteria |
|------------------------|----------------|--|
| Panel General Chipping | Minor | <p> $X > 6 \text{ mm}$ (Along with Edge) $Y > 1 \text{ mm}$ (Perpendicular to edge) </p>  |

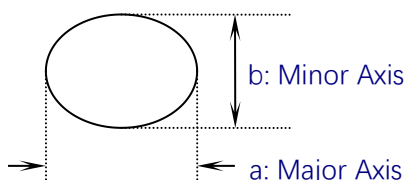
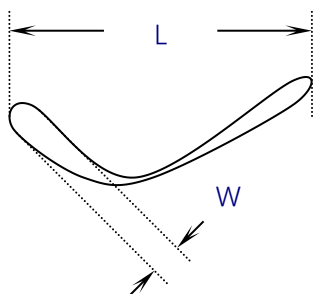
6.3.2 Cosmetic Check (Display Off) in Active Area

It is recommended to execute in clear room environment (class 10k) if actual in necessary.

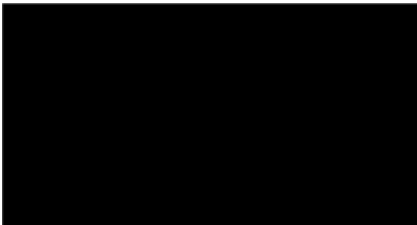
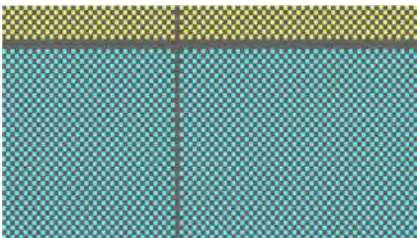
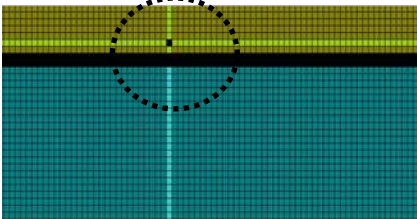
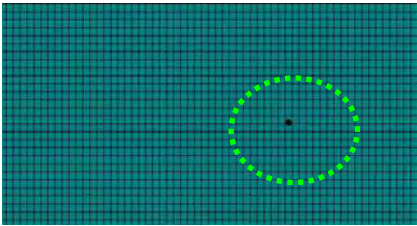
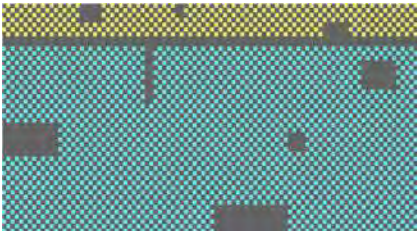
| Check Item | Classification | Criteria |
|---|----------------|---|
| Any Dirt & Scratch on Polarizer's Protective Film | Acceptable | Ignore for not Affect the Polarizer |
| Scratches, Fiber, Line-Shape Defect (On Polarizer) | Minor | $W \leq 0.1$ Ignore $W > 0.1$ $L \leq 2$ $n \leq 1$ $L > 2$ $n = 0$ |
| Dirt, Black Spot, Foreign Material, (On Polarizer) | Minor | $\Phi \leq 0.1$ Ignore $0.1 < \Phi \leq 0.25$ $n \leq 1$ $0.25 < \Phi$ $n = 0$ |
| Dent, Bubbles, White spot (Any Transparent Spot on Polarizer) | Minor | $\Phi \leq 0.5$ → Ignore if no Influence on Display $0.5 < \Phi$ $n = 0$  |
| Fingerprint, Flow Mark (On Polarizer) | Minor | Not Allowable |

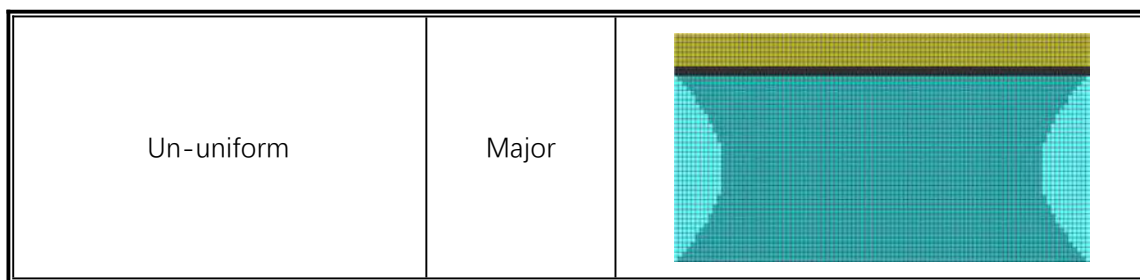
* Protective film should not be tear off when cosmetic check.

** Definition of W & L & Φ (Unit: mm): $\Phi = (a + b) / 2$



6.3.3 Pattern Check (Display On) in Active Area

| Check Item | Classification | Criteria |
|---------------|----------------|--|
| No Display | Major |  |
| Missing Line | Major |  |
| Pixel Short | Major |  |
| Darker Pixel | Major |  |
| Wrong Display | Major |  |



7. Precautions When Using These OEL Display Modules

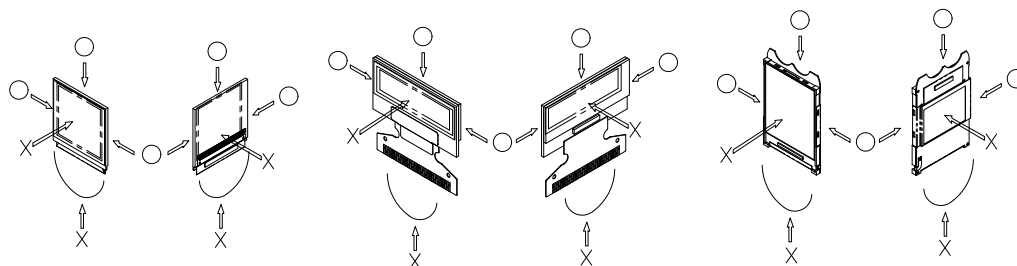
7.1 Handling Precautions

- 1) Since the display panel is being made of glass, do not apply mechanical impacts such as dropping from a high position.
- 2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- 3) If pressure is applied to the display surface or its neighborhood of the OEL display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- 4) The polarizer covering the surface of the OEL display module is soft and easily scratched. Please be careful when handling the OEL display module.
- 5) When the surface of the polarizer of the OEL display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
 - * Scotch Mending Tape No. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.

Also, pay attention that the following liquid and solvent may spoil the polarizer:

- * Water
 - * Ketone
 - * Aromatic Solvents
- 6) Hold OEL display module very carefully when placing OEL display module into the system housing. Do not apply excessive stress or pressure to OEL display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



- 7) Do not apply stress to the driver IC and the surrounding molded sections.
- 8) Do not disassemble nor modify the OEL display module.
- 9) Do not apply input signals while the logic power is off.
- 10) Pay sufficient attention to the working environments when handing OEL display modules to prevent occurrence of element breakage accidents by static electricity.
 - * Be sure to make human body grounding when handling OEL display modules.
 - * Be sure to ground tools to use or assembly such as soldering irons.
 - * To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
 - * Protective film is being applied to the surface of the display panel of the OEL display module. Be careful since static electricity may be generated when exfoliating the protective film.
- 11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OEL display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5).
- 12) If electric current is applied when the OEL display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

7.2 Storage Precautions

- 1) When storing OEL display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. and, also, avoiding high temperature and high humidity environment or low temperature (less than 0 ° C) environments. (We recommend you to store these modules in the packaged state when they were shipped from ZhongJingYuan technology Co.,Ltd.)

At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.

- 2) If electric current is applied when water drops are adhering to the surface of the OEL display module, when the OEL display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

7.3 Designing Precautions

- 1) The absolute maximum ratings are the ratings which cannot be exceeded for OEL display module, and if these values are exceeded, panel damage may be happen.
 - 2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible.
 - 3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}). (Recommend value: 0.5A)
 - 4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
 - 5) As for EMI, take necessary measures on the equipment side basically.
 - 6) When fastening the OEL display module, fasten the external plastic housing section.
 - 7) If power supply to the OEL display module is forcibly shut down by such errors as taking out the main battery while the OEL display panel is in operation, we cannot guarantee the quality of this OEL display module.
 - 8) The electric potential to be connected to the rear face of the IC chip should be as follows: SSD1306
- * Connection (contact) to any other potential than the above may lead to rupture of the IC.

7.4 Precautions when disposing of the OEL display modules

- 1) Request the qualified companies to handle industrial wastes when disposing of the OEL display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

7.5 Other Precautions

- 1) When an OEL display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.
Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module.
- 2) To protect OEL display modules from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the OEL display modules.
 - * Pins and electrodes
 - * Pattern layouts such as the FPC

- 3) With this OEL display module, the OEL driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this OEL driver is exposed to light, malfunctioning may occur.
 - * Design the product and installation method so that the OEL driver may be shielded from light in actual usage.
 - * Design the product and installation method so that the OEL driver may be shielded from light during the inspection processes.
- 4) Although this OEL display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. It therefore is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
- 5) We recommend you to construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise.

Warranty:

The warranty period shall last twelve (12) months from the date of delivery. Buyer shall be completed to assemble all the processes within the effective twelve (12) months. ZhongJingYuan technology Co.,Ltd. shall be liable for replacing any products which contain defective material or process which do not conform to the product specification, applicable drawings and specifications during the warranty period. All products must be preserved, handled and appearance to permit efficient handling during warranty period. The warranty coverage would be exclusive while the returned goods are out of the terms above.

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