# Surenoo HDMI Display Module Series

# Model No.: SHN080A-8001280 USER MANUAL

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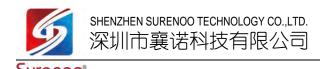
Shenzhen Surenoo Technology Co.,Ltd. www.surenoo.com

# **Reference Links**

**Surenoo HDMI Display Module Selection Guide** 

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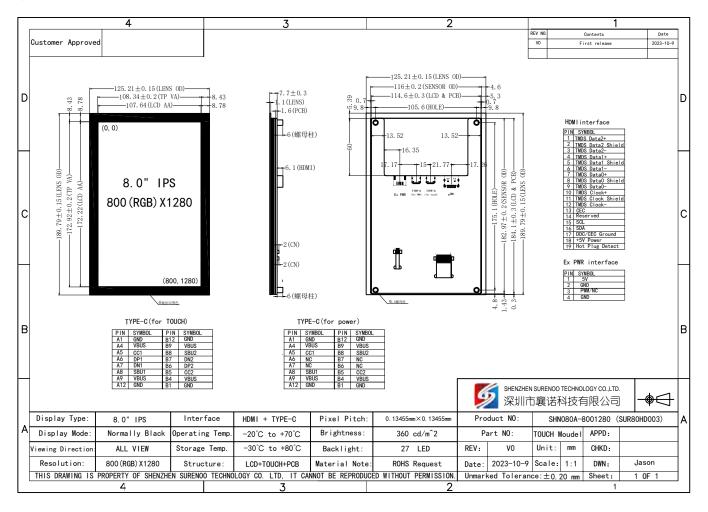
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# 1, GENERAL INFORMATION

Item of general information	Contents	Unit
LCD Display Size (Diagonal)	8.0	inch
LCD Display Type	TFT/TRANSMISSIVE	-
LCD Display Mode	Normally Black	-
Recommended Viewing Direction	ALL VIEW	o'clock
LCM Module size (W×H×T)	125.21×189.79×7.70	mm
Active area (W×H)	107.64×172.22	mm
Number of pixels (Resolution)	800(RGB)×1280	pixel
Pixel pitch (W×H)	0.13455×0.13455	mm
Color Pixel Arrangement	RGB Stripe	-
LCD Driver IC	-	-
Interface Type	HDMI + Type-C	-
Color Numbers	16.7M	-
Backlight Type	White LED	-

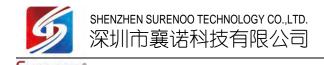
## 2. EXTERNAL DIMENSIONS



#### 3 ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
LCD supply voltage	VDD	5.0	5.0	V
Operating temperature	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	Tst	-30	80	°C
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings mean the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.



## 4 TOUCH CHARACTERISTICS

Item of CTP	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	800 × 1280	pixel	-
Surface Hardness	6Н	-	-
Transparency	≥82%	-	-
Driver IC	GT9271	-	-
Interface Type	USB	-	-
Support Points	5	-	-

## **5. ELECTRO-OPTICAL CHARACTERISTICS**

Item of electro-op	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0.0	ı	ı	35	ms	FIG 1.	4
Contrast F	Contrast Ratio		θ=0□ ∅=0□	1	600	1	ı	FIG 2.	1
Luminance un	iformity	δWHITE	∞-0□ Ta=25°C	1	80	Ī	%	FIG 2.	3
Surface Lum	inance	Lv	1.00	-	350	-	cd/m2	FIG 2.	2
CIE (x, y)	White	White x	θ=0□ ∅=0□	-	0.306	-		FIG 2.	5
chromaticity	white	White y	Ta=25°C	ı	0.360	ı	-	FIG 2.	3
	Ø=90(1	2 o'clock)		-	80	-	deg		
Viewing	Ø=270(	6 o'clock)	CD > 10	-	80	-	deg	EIC 2	
angle range	Ø=0(3	o'clock)□	CR ≥ 10	ı	80	Ī	deg	FIG 3.	6
	Ø=180(	9 o'clock)		-	80	-	deg		
NTSC ratio		-	-	-	60	-	%	-	-

**Note 1.** Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast \ Ratio(CR) = \frac{Average \ Surface \ Luminance \ with \ all \ white \ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average \ Surface \ Luminance \ with \ all \ black \ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$ 

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

**Note 3.** The uniformity in surface luminance (δWHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

 $\delta \text{WHITE} = \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$ 

**Note 4.** Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

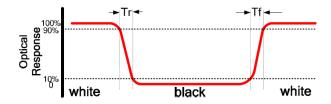
**Note 5.** CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

**Note 7.** For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

**Note 8.** For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

#### FIG.1. The definition of Response Time



## FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance

## uniformity, CIE (x, y) chromaticity

A: H/6; B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

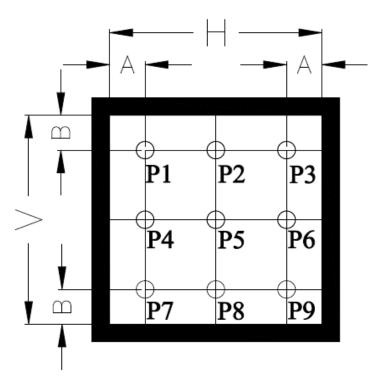
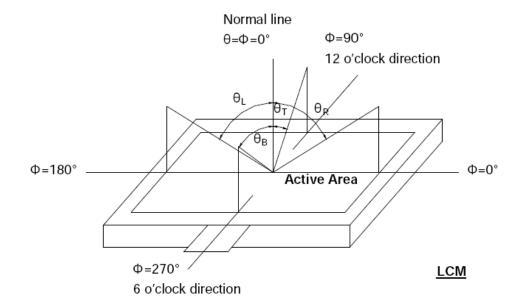


FIG.3. The definition of viewing angle





# 6. INPUT TIMING

Donomoton	Cross had		T 1\$4		
Parameter	Symbol	Min.	Тур.	Max.	Unit
DCLK frequency@ Frame rate=60Hz	DCLK	-	71.76	-	MHz
Horizontal display area	thd		800		DCLK
1 Horizontal Line	th	-	920	-	DCLK
HSYNC pulse width	HSYNC pulse width thpw - 20		20	-	DCLK
HSYNC Back Porch (Blanking)	YNC Back Porch (Blanking) thb		50	-	DCLK
HSYNC Front Porch	thfp	-	50 -		DCLK
Vertical display area	tvd		1280		Н
VSYNC period time	tv	- 1300 -		-	Н
VSYNC pulse width	VSYNC pulse width tvpw - 2		-	Н	
VSYNC Back Porch (Blanking)	VSYNC Back Porch (Blanking) tvb - 10 -		-	Н	
VSYNC Front Porch	tvfp	-	8	-	Н

## 7, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

## A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- ➤ Missing segments;
- Glass crack;
- Current is twice higher than initial value.

#### B. Remark:

- ➤ The test samples should be applied to only one test item.
- ➤ Sample size for each test item is 5~10pcs.
- ➤ Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

#### 8, INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

#### 8.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

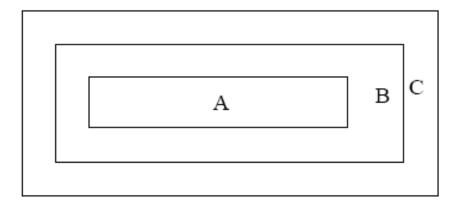
Minor defect: AQL 1.5

#### 8.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60  $\pm 15$ % RH)

## 8.3 Definition of Inspection Item.

## **A.** Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

## **B** Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under
	black pattern.
D. 1. 1.4	Dots appear dark and unchanged in size in which LCD panel is displaying
Dark dot	under pure red, green, blue picture, or pure whiter picture.

## 8.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

## 8.5 Minor Defect

Item No.	Items to be inspected		Inspection standard							Classification of defects			
			Zone				Accepta	able (	Qty				
							A+B						
					4.3~	$4.3 \sim 7$ " $7 \sim 10.1$ " $> 10.1$		>10.1"	С				
		Br	ight pixel do	t	1		2		3				
	Bright dot	D	ark pixel dot		4		4		4	Acc			
1	/dark dot	2brigh	nt dots adja	cent	0		0		0	Acceptable	Minor		
	defect	2dark	dots adjac	cent	0		0		0	ıble			
		Total bri	ght and da	rk dots	5		6		7				
		Note: Mir	imum dist	ance bet	ween	defec	tive dots	s is m	ore than	5mm;			
		Pixel dots	' function	is norma	ıl, but	brigh	it dots ca	used	by foreig	gn			
		material a	nd other re	asons ar	e judg	ged by	y the dot	defe	ct of 5.2.				
			Zone		Acceptable Qty								
					A+B								
	D . 1 C .	Size(mm		4.3"~	~7"	7~	~10.1"	>	10.1"	С			
	Dot defect	Dot defect	Dot defect	Ф \$	≤0.2	Accept	table	Acc	eptable	Acc	eptable	Acc	
2	$\bigcup \bigvee_{\lambda}$	0.2<Ф≤0.5		4			5		6	Acceptable	Minor		
	<del>&lt;</del> →	Φ>	>0.5	0			0		0	le			
	$\Phi = (x+y)/2$	Note:  1. Minimum distance between defective dots is more than 5 mm;  2. The quantity of defect is zero in operating condition.						m;					
			Zone			Ac	ceptable	Qty					
		Size (mn	n)			A	A+B						
3	Linear	Length	Width	4.3"~	~7"	7~	~10.1"	>	10.1"	С	Minor		
3	defect	Ignore	W≤0.05	Accept	table	Acc	eptable	Acc	ceptable	Ac	MILLOL		
			L≤5.0	0.05 < W≤0.1	4			5		6	Acceptable		
			W>0.1	0			0		0	е			
			-	•				•					

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Display	lu .							
		5.4.1 Pola	rizer Posit	ion				
		(i) Shiftir						
		dimension	1.					
		( ii ) Inco	mplete cov	ering of the vi	ewing area du	e to shifting is	s not	
		allowed.	•					
		5.4.2 Dirt	on polariz	er				
			_		ould be accept	able.		
				& Air bubble	1			
			Zone		Acceptable	Otv		
					A+B	<del>(3)</del>		
		Size(mm	n)	4.3"~7"	7~10.1"	>10.1"	С	
			≤0.2	Acceptable	Acceptable	Acceptable		
		Ψ	≥0.2	Ассеріавіе	Ассеріавіе	Acceptable	Ac	
		0.2<	Ф ≤ 0.5	4	5	6	Acceptable	
	Polarizer	Φ.	>0.5	0	0	0	able	
4	defect	Ψ,	<b>-0.3</b>	U	U	U	()	Minor
	defect	5.4.4 Pol	arizer scra	atch				
		(i) If the	polarizer s	scratch can b	e seen after c	over assemb	ling	
		or in the	operating	condition, ju	dge by the li	near defect o	of 5.3.	
		( ii )If the						
		condition						
		Zone Size (mm)			Acceptable			
					Tiecopiacio	Ψ9		
				A+B				
		Length	Width	4.3"~7"	7∼10.1"	>10.1"	С	
		Ignore	W≤0.05	Acceptable	Acceptable	Acceptable	Α	
		1.0 <l< td=""><td>0.05&lt;</td><td>_</td><td>_</td><td></td><td>Acceptable</td><td></td></l<>	0.05<	_	_		Acceptable	
		≤5.0	W≤0.20	4	5	6	otab	
		L>5.0	W>0.2	0	0	0	le	
			<u> </u>	l	<u>I</u>			+
	MURA	Using	3% ND filt	ter, it's NG if	it can be seen	in R,G,B picto	ıre.	
5								
3								Minor
	White/Black	V	isible unde	r: ND3%: E	$0 \le 0.15$ mm, A	acceptable:		
	dot (MURA)	0.15						
				<i>y</i> - · · ·				
		<u> </u>						

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		(i) Crack				
		Cracks are	Minor			
6	Glass defect		$\frac{z}{Y}$ $\leq 3.0$	Z Not more than the thickness of glass minal shall not be allowed perimeter seal.	Acceptable N≤3 d to extend	Minor
		X ≤1.5	Y ≤1.5	Z  Not more than the thickness of glass able to the upper glass of	Acceptable N≤4 FLCD.	Minor

## 8.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
		No soldering missing	Major
3	Soldering defects	No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil ( $\Phi$ 0.5 mm or more) on substrate	Minor
4	Resist Haw Oil PCD	pattern is not allowed	IVIIIOI
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ0.2mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor
11	3. Chips	(3/2) H ≥h ≥(1/2) H	Minor
		a. The spacing between solder ball and the conductor or solder pad h $\geq$ 0.13 mm. The diameter of solder ball d $\leq$ 0.15 mm.	Minor
	4. Solder ball/Solder splash	b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major